Heads : Status update

Heads at 33c3 (2016!!!)



Heads today

[heads-tests] QEMU	
qemu-coreboot-fbwhiptail-tpm1-hotp Heads Boot Menu	
2023-02-01 16:39:28 UTC TOTP: 593928 HOTP: Success	
Default boot	
Refresh TOTP/HOTP	
Options>	
System Info	
Power Off	

What will we be talking about

Plan for today

- Who am I?
- What is Heads
- Why Heads
- What's new?
- What's next?

Thierry Laurion

- Insurgo Open Technology founder and CEO.
- Former Security Analyst/Psychology Bachelor/Security Researcher and Developer.
- Now freedom defender as a open source firmware researcher/developer/integrator.
- Past collaborator to Libreboot, QubesOS contributor and Heads collaborator/reviewer.
- Currently main Heads maintainer.
- Started Insurgo Open Technologies in 2017.
- Made the PrivacyBeast X230 certified by QubesOS in July 2019.
- NInet grantee for the Accessible Security project in April 2019.
- NInet grantee once again for Authenticated Heads (Heads-OpenPGP) project.

Insurgo's mission is to **facilitate accessibility to security and confidentiality** to the masses.



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What is Heads?

- Heads as a runtime environment
- Heads as a build system

Heads as runtime environment



Heads as a runtime environment

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coreboot

- Hardware initialization
- Heads as linux's "bootloader" (kexec)
- Heads: linux as a coreboot payload
 - linux Kernel
 - linux initrd (initramfs)
 - Contain standard linux tools
 - Enforced security policies (shell scripts)

Heads – coreboot native hardware init

- Openness/ownership/auditability of a Heads' coreboot supported platform depends on its coreboot's blobs requirements
- Native hardware initialization depends on arch + chipset
 - On x86: Intel Ivy bridge/ Sandy bridge : all native (no blobs) (watch for Haswell: native ram init coming)
 - On x86: AMD Fam15h: all native (no blobs)
 - KGPE-D16 (coreboot 4.11 last official supported version)
 - Dasharo/coreboot (based on coreboot 4.16)
 - Power9 : Talos II: all native (no blobs)
- More info: https://github.com/osresearch/heads/issues/692

Closed source firmware / BIOS Supply chain



Heads OSFW

- Coreboot source code (and faith in its platform's blobs dependencies)
- Linux kernel (version + config)
- Linux tools (modules dependencies version+config) of a board configuration

Let's note that the linux kernel has:

- extremely limited built in modules/dependencies (linux config for a board)
 - drivers built as modules are loaded on demand
 - USB drivers needed for HOTP USB security dongle
 - USB keyboard not present unless no PS2
 - USB HID not present unless no PS2
- Drivers compiled as modules are measured prior of being loaded (TPM : PCR5)
- consequently, a default boot
 - won't release TPM NV sealed disk unlock key if cannot be unsealed
 - PCRs will mismatch from sealed and will not unseal

Heads as a runtime environment

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Heads as a linux bootloader

- A bootloader is
 - Drivers to be able to deal with I/O
 - Parser for OS configs
 - Pass control to the OS
- Bootloader sits between BIOS and OS to be booted
- Linux and scripts can do exactly the same without duplicating hardware init nor extending trusted code base...
 - Busybox (shell)
 - Cryptsetup, lvm, tpm toolstack...

Heads as a linux bootloader

HEADS Options	Boot Options
Boot Options>	Select A Boot Option
TPM/TOTP/HOTP Options>	Show OS boot menu
Update checksums and sign all files in /boot	USB boot
Change configuration settings>	
Flash/Update the BIOS>	Ignore tampering and force a boot (Unsafe!)
GPG Options>	< Return to main menu
Select your boot option	Confirm boot details
e boot option [1-3, a to abort]:	Confirm the boot details for Debian GNU/Linux:
GNU/Linux	kernel /vmlinuz-5.10.0-20-amd64
GNU/Linux,_with_Linux_5.10.0-20-amd64	Make default
GNU/Linux,_with_Linux_5.10.0-20-amd64_(recovery_mode)	Boot one time

Choose

Debiar

Debiar

New value of PCR[7]: 088c85f27ed7fdf5ce03ddcb278e78ce830babe9 Loading /.gnupg/trustdb.gpg from CBFS New value of PCR[7]: ad01634c1ab7f05f2d981cab4db7e7821c9cb836 ***** Normal boot: /bin/gui-init 7.814495] EXT4-fs (uda1): mounting ext2 file sustem using the ext4 subsustem 7.854843] EXT4-fs (vda1): mounted filesystem without journal. Opts: (null) 8.186793] random: tpm: uninitialized urandom read (20 butes read) 8.254785] random: shred: uninitialized urandom read (312 butes read) 8.278632] random: shred: uninitialized urandom read (312 butes read) sh: argument expected +++ Found verified kexec boot params 198.008678] random: fast init done gpg: Signature made Fri Feb 3 02:43:44 2023 UTC using RSA key ACF4B7893D4D05C8F18069BAE7B4A71658E36A93 gpg: Good signature from "Insurgo Technologies Libres / Open Technologies <insurgo@riseup.net>" [ultimate] aka "[ipeg image of size 9521]" [ultimate] +++ Found verified kexec boot params +++ Scanning for unsigned boot options +++ Checking verified boot hash file 207.9326231 random: crng init done 207.9451611 random: 7 urandom warning(s) missed due to ratelimiting +++ Verified boot hashes : 000ehaddecaf00000022 ∕tmp/counter-0: OK +++ Checking verified default boot hash file +++ Verified default boot hashes +++ Executing default boot for Debian GNU/Linux: New value of PCR[6]: 897c3968a1eb7f99286b65f3f35c772a38ac7863 Enter unlock password (blank to abort): New value of PCR[4]: 11c4ecaf31383e76686cc64f0c1dd88bd918ea3d +++ Building initrd 96096+1 records in 96097+0 records out 49201664 bytes (46.9MB) copied, 3.094374 seconds, 15.2MB/s /boot/kexec initrd crupttab overrides.txt found... Preparing initramfs crypttab overrides as defined under /boot/kexec initrd crypttab overrides.txt to be injected through cpio at next kexec call... initramfs's cruptroot/crupttab will be overriden with vda5 crupt UUID=2fab863e-9858-4b5f-a217-7cf000d5649e /secret.keu luks.discard Loading the new kernel: kexec -l /boot/umlinuz-5.10.0-20-amd64 --initrd=/tmp/secret/initrd.cpio --append="root=/deu/mapper/debian--ug-root ro console=ttyS0 console=tty systemd.zram=0" Starting the new kernel

[heads-tests] OFMU

In action: gui-init boot policy:

Loading /.gnupg/pubring.kbx from CBFS

- detach signature against public key + hash validation. TPM NV auth + unsealing LUKS

- cpio constructed with parsed OS's crypttab + TPM disk unlock key inserted at kexec (cpio)



Sda1 is flat ext4 partition with ISO+detach signature (iso.asc) put there. "media-scan" merged recently





Detached signature validation against ROM's OS's distro signing public key Then grub entree selection parsing + kexec

		Jan 27 20:01		0	•(1)
	Shutdown		Start Tails		
	١	Welcome to Tails!			
	Language & Region 🕜				
	🔊 Language		English - United States		
and the second	E Keyboard Layout		English (US)		
	Formats		United States - English		
	Persistent Storage You can save some of Storage on your Tails passwords, and so on	your files and configuratio USB stick: your documents	n in an encrypted Persistent 5, browser bookmarks, Wi-Fi Create Persistent Storage		
	Additional Settings ()				
	The default settings are safe in button below.	most situations. To add a	custom setting, press the "+"		
	+				
				3	

Heads as a runtime environment

	[heads-tests] QEMU	
	gemu-coreboot-fbwhiptail-tpm1-hotp Heads Boot Menu	
202	23-02-01 16:39:28 UTC	
	11: 293920 HUIF: SUCCESS	
	Default boot	
	Refresh TOTP/HOTP	
	Options>	
5	System Info	
F	Power Off	
_		

(gui-init through fbwhiptail above)

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Heads as a build system

- Heads is basically a 'Make' project
 - Global Makefile https://github.com/osresearch/heads/blob/master/Makefile
 - make BOARD=board_name module_name.statement options
 - Boards https://github.com/osresearch/heads/tree/master/boards
 - Existing modules (compilable software): https://github.com/osresearch/heads/tree/master/modules
 - Patches to be applied after module verification + extraction: https://github.com/osresearch/heads/tree/master/patches
 - Produces
 - Artifacts : creboot rom(s) images stitching the following (but produced independently)
 - BzImage (compiled kernel + in-kernel modules)
 - Initrd.cpio.xz
 - tools.cpio (compiled modules stripped binaries)
 - modules.cpio (compiled as modules kernel drivers to be loaded on demand)
 - heads.cpio (scripts and config files generated at build time linked to board config and https://github.com/osresearch/heads/tree/master/initrd content
 - Hashes.txt file containing individual packed files, cpios, initrd.cpio.xz and coreboot roms

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Why Heads



- Outside of coreboot's "minimalist" mandate:
 - Linux as its bootloader
 - Linux kernel enumerates devices
 - Linux supports of peripherals/buses
 - Linux support of filesystems
 - Linux kernel setups of IOMMU
 - Linux permits Kexec'ing into final OS
 - Standard linux tools in initrd (**add your own module if missing**)
 - TPM toolstacks (sealing/unsealing of secrets)
 - Bash scripts defines the boot policies launched by init
 - gui-init: validates/creates detached signatures, hashes, LUKS headers
 - Maximized roms takes back all ME neutered freed space, unlocks firmware descriptor, permitting internal full firmware upgrades!

Why Heads

- Extensive TPM usage
 - Coreboot measured boot mode extends TPM PCR2 (no DRTM as of now on supported platforms. Might change with T440p and other newer platforms)
 - Heads extends others
 - PCR4: Boot mode (0 during /init, then recovery or normal-boot)
 - PCR5: Heads Linux kernel modules
 - PCR6: Drive LUKS headers
 - PCR7: Heads user-specific files stored in CBFS (config.user, GPG keyring, etc).
 - Why important?
 - Sealing secrets in TPM NV memory
 - TOTP/HOTP sealed secret (based on PCRs 0-4) : Firmware integrity attestation
 - TPM Disk Unlock Key (based on PCRs 0-7) sealed secret with custom passphrase: releases key to OS without you having to type passphrase to that OS

gui-init policy + TPM released Disk unlock key

[heads-tests] QEMU

New value of PCR[7]: 088c85f27ed7fdf5ce03ddcb278e78ce830babe9 Loading /.gnupg/trustdb.gpg from CBFS New value of PCR[7]: ad01634c1ab7f05f2d981cab4db7e7821c9cb836 ***** Normal hoot: /hin/qui-init 7.814495] EXT4-fs (uda1): mounting ext2 file system using the ext4 subsystem 7.854843] EXT4-fs (vda1): mounted filesustem without journal. Opts: (null) 8.186793] random: tpm: uninitialized urandom read (20 butes read) 8.254785] random: shred: uninitialized urandom read (312 butes read) 8.278632] random: shred: uninitialized urandom read (312 bytes read) sh: argument expected +++ Found verified kexec boot params 198.008678] random: fast init done gog: Signature made Fri Feb 3 02:43:44 2023 UTC using RSA key ACF4B7893D4D05C8F18069BAE7B4A71658E36A93 Good signature from "Insurgo Technologies Libres / Open Technologies (insurgo@riseup.net)" [ultimate] aka "[ipeg image of size 9521]" [ultimate] gpg : +++ Found verified kexec boot params +++ Scanning for unsigned boot options +++ Checking verified boot hash file 207.932623] random: crng init done 207.945161] random: 7 urandom warning(s) missed due to ratelimiting +++ Verified boot hashes 0: 000ebaddecaf0000022 /tmp/counter-0: OK +++ Checking verified default boot hash file +++ Verified default boot hashes +++ Executing default boot for Debian GNU/Linux: New value of PCR[6]: 897c3968a1eb7f99286b65f3f35c772a38ac7863 Enter unlock password (blank to abort): New value of PCR[4]: 11c4ecaf31383e76686cc64f0c1dd88bd918ea3d +++ Building initrd 96096+1 records in 96097+0 records out 49201664 butes (46.9MB) copied, 3.094374 seconds, 15.2MB/s /boot/kexec initrd crupttab overrides.txt found... Preparing initramfs crypttab overrides as defined under /boot/kexec_initrd_crypttab_overrides.txt to be injected through cpio at next kexec call... initramfs's cryptroot/crypttab will be overriden with vda5_crypt UUID=2fab863e-9858-4b5f-a217-7cf000d5649e /secret.key luks,discard Loading the new kernel: kexec -l /boot/umlinuz-5.10.0-20-amd64 --initrd=/tmp/secret/initrd.cpio --append="root=/deu/mapper/debian--ug-root ro console=ttyS0 console=tty systemd.zram=0" Starting the new kernel

Loading /.gnupg/pubring.kbx from CBFS

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What's new

- Maximized boards vs Legacy boards, or how to dodge blob redistribution legal limitations
- Whiptail/FBWhiptail: one graphical interface (GUI) to rule them all
- OEM Factory reset/Re-Ownership wizard upstreamed
- QEMU/KVM board configurations with swtpm and USB Security dongle support to ease development/testing

OEM/Legacy BIOS

Flash layout

There's one 8MiB and one 4 MiB flash which contains IFD, GBE, ME and BIOS region. These two flash ICs appear as a single 12MiB when flashing internally. On Lenovo's UEFI the EC firmware update is placed at the start of the BIOS region. The update is then written into the EC once.



Intel Firmware Descriptor (IFD): Locked. Describes GBE ME BIOS sizes here! IFD, Intel ME and BIOS regions : cannot be modified/unlocked but externally user@heads-tests:~/heads\$ cat boards/x230-hotp-verification/x230-hotp-verification.config /# Configuration for a x230 with HOTP (Nitrokey/Purism USB Security dongle enabled HOTP support) # running Qubes 4.1 and other OSes.

Deactivated to fit in coreboot's CONFIG_CBF5_SIZE=0x7000000 : # dropbear support(ssh client/server) # e1000e (ethernet driver)

Addition vs standard x230 board config: # HOTP.KEY: HOTP challenge for currently supported USB Security dongles export CONFIG_COREBOOT-y export CONFIG_COREBOOT_VERSION=4.13 export CONFIG_LINUX,VERSION=4.4.62

CONFIG_COREBOUT_CONFIG=config/coreboot-x230-hotp-verification.config CONFIG_LINUX_CONFIG=config/linux-x230-legacy.config

#Additional hardware support CONFIG_LINUX_USB=y CONFIG_LINUX_E1000E=n

CONFIG_CRYPTSETUP2-y CONFIG_FLASHROM-y CONFIG_FLASHROM-y CONFIG_CASHROM-y CONFIG_CRC2-y CONFIG_CRC2-y CONFIG_CNTL_LIND=y CONFIG_NEDTLS-y CONFIG_MEDTLS-y CONFIG_NEDTLS-y

#Remote attestation support #TFM based requirements export CONFIG_TPM=y CONFIG_QRENCODE=y CONFIG_GRENCODE=y CONFIG_THMTOTP=y #HOTP based remote attestation for supported USB Security dongle #With/Without TFM support CONFIG_HOTPKEY=y

#Nitrokey Storage admin tool CONFIG NKSTORECLI=n

#GUI Support #Console based Mhiptail support(Console based, no FB): CONFIG_SLANG=y CONFIG_NENT=y #FBMhiptail based (Graphical): #CONFIG_CAIR0=y #CONFIG_FBMHIPTAIL=y

#Additional tools: #SSH server (requires ethernet drivers, eg: CONFIG_LINUX_E1000E) CONFIG_DROPBEAR-n #Ethernet driver (Heads only) CONFIG_LINUX_E1000E=n

export CONFIG BOOTSCRIPT=/bin/gui-init export CONFIG_BOOT REQ_HASH=n export CONFIG_BOOT REQ_HASH=n export CONFIG_BOOT KENNEL_ADD="initel_iommu=on intel_iommu=igfx_off" export CONFIG_BOOT_KENNEL_RENOVE="quiet" export CONFIG_BOOT_DEV='/dev/sda1" export CONFIG_BOOT_DEV='/dev/sda1" export CONFIG_BOARD_NAME="Thinkpad X230-hotp" export CONFIG_FLASHEON OPTIONS=-:force --noverify-all -p internal --ifd --image bios"

This board has two SPI flash chips, an 8 MB that holds the IFD, # the ME image and part of the coreboot image, and a 4 MB one that # has the rest of the coreboot and the reset vector.

OEM/Legacy BIOS

x230-flash/x230-hotp-verification (legacy example)

```
export CONFIG_FLASHROM_OPTIONS="--force --noverify-all -p internal --ifd --image bios"
# This board has two SPI flash chips, an 8 MB that holds the IFD,
# the ME image and part of the coreboot image, and a 4 MB one that
# has the rest of the coreboot and the reset vector.
#
# Only flashing to the bios region is safe to do. The easiest is to
# flash internally when the IFD is unlocked for writing, and x230-flash
# is installed first.
```

• Contains unlocked + modified IFD

user@heads-tests:~/heads/blobs/xx30\$ ~/heads/build/x86/coreboot-4.13/util/ifdtool/ifdtool -f layout.txt ifd.bin
File ifd.bin is 4096 bytes
Wrote layout to layout.txt
user@heads-tests:~/heads/blobs/xx30\$ cat layout.txt
00000000:00000fff fd
0001b000:00bfffff bios
00003000:0001afff me
00001000:00002fff gbe

- 00bfffff 0001b000 = BE4FFF
 - Can be maximized even more since should match coreboot's CBFS_SIZE!
- Contains Neutered ME
- Contains generated GBE (per specs)

- Blobs redistribution legal issue dodging
 - Github contains download+extraction code
 - CircleCI downloads and stitches ROMs
 - No blobs are hosted

user@heads-tests:~/heads/blobs/xx30\$ cat download clean me.sh #!/bin/bash function printusage { echo "Usage: \$0 -m <me cleaner>(optional)" BLOBDIR="\$(cd "\$(dirname "\${BASH SOURCE[0]}")" && pwd)" if ["\$#" -eq 0 1: then printusage: fi while getopts ":m:" opt; do case \$opt in m) if [-x "\$OPTARG"]; then MECLEAN="\$0PTARG" fi :: esac done FINAL ME BIN SHA256SUM="c140d04d792bed555e616065d48bdc327bb78f0213ccc54c0ae95f12b28896a4 \$BL0BDIR/me.bin" ME_EXE_SHA256SUM="f60e1990e2da2b7efa58a645502d22d50afd97b53a092781beee9b0322b61153_g1rg24ww.exe" ME8 5M PRODUCTION SHA256SUM="821c6fa16e62e15bc902ce2e958ffb61f63349a471685bed0dc78ce721a01bfa app/ME8 5M Production.bin" if [-z "\$MECLEAN" 1: then MECLEAN= command -v \$BLOBDIR/../../build/coreboot-*/util/me cleaner/me cleaner.py 2>&1|head -n1 if [-z "\$MECLEAN" 1; then echo "me cleaner.py required but not found or specified with -m. Aborting." exit 1: fi fi echo "### Creating temp dir" extractdir=\$(mktemp -d) cd "Sextractdir" echo "### Downloading https://download.lenovo.com/pccbbs/mobiles/glrg24ww.exe..." wget https://download.lenovo.com/pccbbs/mobiles/glrg24ww.exe || { echo "ERROR: wget not found" && exit 1; } echo "### Verifying expected hash of glrg24ww.exe" echo "\$ME EXE SHA2565UM" | sha256sum --check || { echo "Failed sha256sum verification on downloaded binary..." & exit 1; } echo "### Extracting glrg24ww.exe..." innoextract ./glrg24ww.exe || { echo "Failed calling innoextract. Tool installed on host?" && exit 1;} echo "### Verifying expected hash of app/ME8 5M Production.bin" echo "\$ME8 5M PRODUCTION SHA256SUM" | sha256sum --check || { echo "Failed sha256sum verification on extracted binary..." && exit 1; } echo "###Applying me cleaner to neuter+deactivate+maximize reduction of ME on \$bioscopy, outputting minimized ME under \$BLOBDIR/me.bin... " \$MECLEAN -r -t -0 "\$BLOBDIR/me.bin" app/ME8 5M Production.bin echo "### Verifving expected hash of me.bin" echo "\$FINAL ME BIN SHA256SUM" | sha256sum --check || { echo "Failed sha256sum verification on final binarv..." & exit 1; } echo "###Cleaning up...' cd rm -r "\$extractdir'

CircleCI downloads/clean/put blobs in place

② prep_env (6857) - osrese ×	+	
	https://app.circleci.com/pipelines/github/osresearch/heads/511/worki 80% 🏠 🔍 Search 🛛 🛇 🛃 S	() 🛊 🕱 =
🗘 Wiki needs improve 💮 arc	rchive.today-this 🎧 System beep/bell d 🔞 New Tab 💮 archive.org-this 🗅 Work 🗅 Customer 🗅 Active 💮 Low Level PC/Serve 📎	C Other Bookmark
osresearch v tlaurion v	Ownload and neuter xx30 ME (keep generated GBE and extracted IFD in tree) 03 FTPR (0x00180000 - 0x00024a000, 0x000ca000 total bytes): NOT removed 64 NFTP (0x0024a000 - 0x0004a000, 0x0025a000 total bytes): removed	1s [∄ 🕹
Dashboard	65 Removing partition entries in FPT 66 Removing EFFS presence flag 67 Correcting checksum (0xed) 69 Dendies FTDP mediac Nick I	
Projects	09 Reduing FFR modules list 09 UPDATE (LZMA , 0x1cc508 - 0x1cc6c6): removed 70 ROMP (Huffman, fragmented data, ~2 KiB): NOT removed, essential	
11. Insights	71 BUP (Huffman, fragmented data, ~56 KiB): NOT removed, essential 72 KERNEL (Huffman, fragmented data, ~135 KiB): removed	
Organization Settings	73 POLICY (Huffman, fragmented data, ~91 KiB): removed 74 HOSTCOMM (LZMA, 0x1c6c6 - 0x1d343f): removed 75 RSA (LZMA, 0x1d343f - 0x1d872a): removed	
S Plan	76 CLS (LZMA, 0x1d872a - 0x1ddec0); removed 77 TDT (LZMA, 0x1ddec0 - 0x1e45be); removed 78 FTCS (Huffman, fragmented data, ~18 KiB); removed	
Save your credits X	79 ClsPriv (LZMA , 0x1e45be - 0x1e499f): removed 80 SESSMGR (LZMA , 0x1e499f - 0x1f32cb): removed	
failure. Try out the new	81 Relocating FTPR from 0x180000 - 0x24a000 to 0xd00 - 0xcad00 82 Adjusting FPT entry	
fail-fast preview	 Adjusting LUT start offset Adjusting Huffman start offset Adjusting chunks offsets Moving data 	
Notifications	87 The ME minimum size should be 98304 bytes (0x18000 bytes) 88 Truncating file at 0x18000 89 Checking the FTPR RSA signature VALID	
Status OPERATIONAL	90 Done! Good luck! 91 ### Verifying expected hash of me.bin	
Docs	92 /root/project/blobs/xx30/me.bin: OK 93 ###Cleaning up 94 /root/project	
tet Orbs	of Coinclear persived suit and a	

CircleCI stitches the ROM together

Dutation / rimshed Queded Executor / resource class Blanch Co	minit. Author & message								
() 2m 40s / 6d ago X 0s	 268767d, 1c68bef Merge pull request #1296 from tlaurion/new_tails_key 								
STEPS TESTS TIMING ARTIFACTS RESOURCES (NEW)									
arallel runs									
0 Use parallelism to run faster tests Go to Docs 02:39 Parallelism speeds up tests by splitting them across multiple executors. Go to Docs	×								
 Spin up environment 	2s 🔁 🛃								
 Preparing environment variables 	0s [7] J								
Attaching workspace	54s 📑 🚽								
► 📀 Install dependencies	38s [7] 🚽								
🕨 🥑 Make Board	1m 2s [7] 날								
 Output hashes 	0s [7] 🕁								
 Archiving build logs. 	0s کے ج								
 Output build failing logs 	0s کے ج								
 Ø Uploading artifacts 	1s آڙ لي								

CircleCI stitches the ROM together

0	Make Board					1m 2s	[] ₹	
Your o	utput is too large to display in the bro	wser.						
Only th	e last 400000 characters are displaye	d.						
Dowr	load the full output as a file							
		23011					<u></u>	
	rm x230-hotp-maximized usb-kb	/util/cbfstd	ool/fmd scanner	.c x230-	otp-maximized usb-kb/util/cbfstool/fmd parser.c			
	make[1]: Leaving directory '/	root/project	/build/x86/cor	eboot-4.	3'			
	********** 14:06:22-05:00 DONE	E coreboot						
	touch /root/project/build/x86/	/coreboot-4	13/x230-hotp-m	aximized	usb-kb/.build			
	# Use coreboot.rom, because cu	ustom output	t files might r	ot be pr	cessed by cbfstool			
	"/root/project/build/x86/corel	boot-4.13/x2	230-hotp-maximi	zed_usb-	b/cbfstool" "/root/project/build/x86/coreboot-4.13/x236-hotp-maximized_usb-kb/coreboot.rom" print			
	FMAP REGION: COREBOOT							
	Name	Offset	Туре	Size	Comp			
	cbfs master header	0x0	cbfs header	32	none			
	fallback/romstage	08X0	stage	85100	none			
	cpu_microcode_blob.bin	0x14d80	microcode	26624	none			
	fallback/ramstage	0x1b600	stage	97672	none			
	config	0x333c0	raw	848	none			
	revision	0x33780	raw	691	none			
	fallback/dsdt.aml	0x33a80	raw	14615	none			
	Vbt.bin	0x37400	raw	1433	LZMA (4281 decompressed)			
	cmos_layout.bin	0x37a00	cmos_layout	1884	none			
	fallback/postcar	0X38100	stage	25816	none			
	fallback/payload	0x30700	simple err	1330247	Note			
	(empty)	exh5fdce	hoothlock	4340000				
	*********** 14-06-22-05-80 TNS	TALL build	/v86/coreboot.	4 13/223	none S.hoto.maximized ush.kh/coreboot rom -> huild/v86/v238.hoto.maximized ush.kh/heads.v230.hoto.maximized ush.kh.v8 3	8-1357-0	1c68bef r	
	if cmpquiet "/root/project	/build/x86/o	oreboot -4, 13/2	230-hotn	maximized ush.kb/coreboot.rom" "/root/project/build/x86/x28.both-maximized ush.kb/beads.x28.both-maximized ush.	kh-v8.2.0	B-1357-01c	
	= 1 cmp = quiet = / for the last work of each a start work of each and the last work of each							
	********** 14:06:22-05:80 DD 8	BMB build/x8	36/x230-hotp-ma	ximized	sb-kb/heads-x230-hotp-maximized_usb-kb-v0.2.8-1357-g1c68bef-bottom.rom			
	dd of=/root/project/build/x86	/x230-hotp-r	naximized usb-	b/heads-	230-hotp-maximized_usb-kb-v0.2.0-1357-u1c68bef-bottom.rom_if=/root/project/build/x86/x230-hotp-maximized_usb-kb/h	eads-x23(9-hotp-max	
	96b33730b63197a5a7fe9bd34795fe	eb603f976000	4018a799b24a0a	6211e8da	/root/project/build/x86/x230-hotp-maximized_usb-kb/heads-x230-hotp-maximized_usb-kb-v0.2.0-1357-g1c68bef-bottom	. rom		
	********* 14:06:22-05:80 DD 4	4MB build/x8	36/x230-hotp-ma	ximized_	isb-kb/heads-x230-hotp-maximized_usb-kb-v0.2.8-1357-g1c68bef-top.rom			
	dd of=/root/project/build/x86/	/x230-hotp-r	naximized_usb-	b/heads-	230-hotp-maximized_usb-kb-v0.2.0-1357-g1c68bef-top.rom if=/root/project/build/x86/x23 <u>0-hotp-maximized_usb-kb/head</u>	s-x230-h(otp-maximi:	
	ed3eea39a0500ccf166842881b196	59d2dc99821	39d18ed794e0ebd	24950082	/root/project/build/x86/x230-hotp-maximized_usb-kb/heads-x230-hotp-maximized_usb-kb-v0.2.0-1357-g1c68bef-top.ro	m)		
786	e2466eb66fd8a76d15abdfd6ef4d8	bc14d64a7f99	97beb31dbec81b8	bd43a049	/root/project/build/x86/x230-hotp-maximized_usb-kb/heads-x230-hotp-maximized_usb-kb-v0.2.0-1357-q1c68bef.rom			

CircleCI keeps artifacts for each built commit (30 davs)

Dashboard F	Project I	Branch Workflow	Job		
All Pipelines >	🖪 heads >	ያ master > ሳያ build_and_	test > • x2	30-hotp-maximized_usb-kb	(6916)
• x230-hotp-	maximize	d_usb-kb 📀 Success			
Duration / Finished	Queued	Executor / Resource Class	Branch	Commit	Author & Message
🛈 2m 40s / 6d ag	jo 🛛 0s	Docker / Large 7 ①	🌮 master	← 268767d, 1c68bef	Merge pull request #1296 from tlaurion/new_tails_key
STEPS TESTS	TIMING A	RTIFACTS RESOURCES NEW			
🝷 💮 Parallel Run	0				
build/x86/x230-hot	tp-maximized_us	b-kb/bzimage b-kb/bashes txt			
build/x86/x230-hot	tp-maximized_us	b-kb/heads-x230-hotp-maximized_	usb-kb-v0.2.0-135	7-g1c68bef-bottom.rom	
build/x86/x230-hot	tp-maximized_usl	b-kb/heads-x230-hotp-maximized_	usb-kb-v0.2.0-135	7-g1c68bef-top.rom	
build/x86/x230-hot	tp-maximized_usl	b-kb/heads-x230-hotp-maximized_	usb-kb-v0.2.0-135	7-g1c68bef.rom	
build/x86/x230-hot	tp-maximized_usl	b-kb/heads.cpio			
build/x86/x230-hot	tp-maximized_usl	b-kb/initrd.cpio.xz			
build/x86/x230-hot	tp-maximized_usl	b-kb/logs.tar.gz			
build/x86/x230-hot	tp-maximized_usl	b-kb/modules.cpio			
build/x86/x230-hot	tp-maximized_usl	b-kb/tools.cpio			

What's new

- Maximized boards vs Legacy boards, or how to dodge blob redistribution legal limitations
- Whiptail/FBWhiptail: one graphical interface (GUI) to rule them all
- OEM Factory reset/Re-Ownership wizard upstreamed
- QEMU/KVM board configurations with swtpm and USB Security dongle support to ease development/testing

Whiptail : console (server/BMC)

▼ [heads-tests] QEMU	
genu-coreboot-whiptail-tpm1 Heads Boot Menu	1
2023-02-03 20106131 UTC TOTP: 905940 I HOTP: N∕A	
d Default boot	
o Options>	
s System Info p Power Off	
 <0k> <cance1></cance1> 	
	1

FBWhiptail: Desktop/Laptops

[heads-tests] QEMU	
qemu—coreboot—fbwhiptail—tpm1 Heads Boot Menu 2023-02-03 20:08:10 UTC TOTP: 537396 HOTP: N/A	
Default boot	
Refresh TOTP/HOTP	
Options>	
System Info	
Power Off	

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This operation will automatically:

- * ERASE the TPM and own it with a password
- * ERASE any keys or passwords on the GPG smart card,

reset it to a factory state, generate new keys

- and optionally set custom PIN(s) * Add the new GPG key to the firmware and reflash it
- * Sign all of the files in /boot with the new GPG key

It requires that you already have an OS installed on a dedicated /boot partition. Do you wish to continue?

Would you like to change the current LUKS Disk Recovery Key passphrase? (Highly recommended if you didn't install the Operating System yourself, so that past provisioned passphrase would not permit to access content. Note that without re-encrypting disk, a backuped header could be restored to access encrypted content with old passphrase) [y/Kl: y

Gould you like to re-encrypt LUKS encrypted container and generate new Disk Recovery key? (Highly recommended if you didn't install the operating system yourself: this would prevent any LUKS backuped header to be restored to access encrypted data) Ly/HJ: m The following security components will be provisioned with defaults or chosen PINs/passwords: LUKS Disk Recovery Key passphrase TPM Ownership password GPG disne PIN GPG User PIN

Would you like to set a single custom password that will be provisioned to previously stated security components? [y/N]: n Would you like to set distinct PINs/passwords to be provisioned to previously stated security components? [y/N]: n

Enter desired replacement for current Disk Recovery Key passphrase (At least 6 characters long): Insurgo Open Technologies

Enter current Disk Recovery Key passphrase (Provisioned at OS installation or by OEM): Insurgo Open Technologies

Hould you like to set custom user information for the GnuPG key? [y/N]: n Hould you like to export your public key to an USB drive? [y/N]: n

Checking for USB Security Dongle...

Detecting and setting boot device ...

Boot device set to /deu/sda1

Reencrypting /dev/sda4 LUKS encrypted drive content with current Recovery Disk Key passphrase...

Provisioned secrets

TPM Owner Password: 12345678 GPG Admin PIN: 12345678 GPG User PIN: 123456

OK.

OEM Factory Reset / Re-Ownership Complete

OEM Factory Reset / Re-Ownership has completed successfully

After rebooting, you will need to generate new TOTP/HOTP secrets when prompted in order to complete the setup process.

Press Enter to reboot.

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QEMU/KVM boards

- Permit easy testing and debugging (debian-12 recommended)
 - After having followed instructions: https://osresearch.net/Emulating-Heads/#comprehensive-test
 - make BOARD=qemu-coreboot-fbwhiptail-tpm2
 PUBKEY_ASC=~/QubesIncoming/Insurgo/Insurgo_2023_pub.asc
 USB_TOKEN=NitrokeyStorage ROOT_DISK_IMG=~/QubesIncoming/heads-tests/root.qcow2
 QEMU_MEMORY_SIZE=1G inject_gpg
 - make BOARD=qemu-coreboot-fbwhiptail-tpm2
 PUBKEY_ASC=~/QubesIncoming/Insurgo/Insurgo_2023_pub.asc
 USB_TOKEN=NitrokeyStorage ROOT_DISK_IMG=~/QubesIncoming/heads-tests/root.qcow2
 QEMU_MEMORY_SIZE=1G run

QEMU/KVM boards

2023-02-04 05:09:47-05:00 INSTALL build/x86/coreboot-fbwhiptail-toml-hotp/coreboot.rom => build/x86/gemu-coreboot-fbwhiptail-toml-hotp/heads-gemu-coreboot-fbwhiptail-toml-hotp-v0.2.0-1359-g411ca09.rom 7bc038ef939b66b3b247093efc0a2813d374a67d453d464c85138fa2eca8aee6 build/x86/gemu-coreboot-fbwhiptail-tpml-hotp/heads-gemu-coreboot-fbwhiptail-tpml-hotp-v0.2.0-1359-g411ca09.rom cp "/home/user/heads/build/x86/gemu-coreboot-fbwhiptail-tpml-hotp/heads-gemu-coreboot-fbwhiptail-tpml-hotp-v0.2.0-1359-a411ca09.rom" \ "/home/user/heads/build/x86/gemu-coreboot-fbwhiptail-tpm1-hotp/heads-gemu-coreboot-fbwhiptail-tpm1-hotp-v0.2.0-1359-g411ca09-gpg-injected.rom" /bin/inject gpg key.sh --cbfstool "/home/user/heads/build/x86/coreboot-4.13/gemu-coreboot-fbwhiptail-tpml-hotp/cbfstool" \ "/home/user/heads/build/x86/gemu-coreboot-fbwhiptail-tpml-hotp/heads-gemu-coreboot-fbwhiptail-tpml-hotp-v0.2.0-1359-g411ca09-gpg-injected.rom" "/home/user/QubesIncoming/Insurgo/Insurgo 2023 pub.asc" Inserting /home/user/OubesIncoming/Insurgo/Insurgo/Insurgo/2023 pub.asc into /home/user/heads/build/x86/gemu-coreboot-fbwhiptail-tpml-hotp/heads-gemu-coreboot-fbwhiptail-tpml-hotp/heads-gemu-coreboot-fbwhiptail-tpml-hotp/heads/gemu-coreboot-fbwhiptail-tpml-hotp/heads/gemu-coreboot-fbwhiptail-tpml-hotp/heads-gemu-coreboot-fbwhiptail-tpml-hotp/heads/gemu-coreboot-fbwhiptailgpg: keybox '/tmp/tmp-inject gpg key.sh-BOV/pubring.kbx' created gpg: /tmp/tmp-inject_gpg_key.sh-BOV/trustdb.gpg: trustdb created gpg: key E784A71658E36A93: public key "Insurgo Technologies Libres / Open Technologies <insurgo@riseup.net>" imported gpg: Total number processed: 1 imported:] apq: gpg: inserting ownertrust of 6 gpg: inserting ownertrust of 6 gpg: inserting ownertrust of 6 gpg: marginals needed: 3 completes needed: 1 trust model: pgp gpg: depth: 0 valid: 3 signed: 0 trust: 0-, 0g, 0n, 0m. 0f. 3u gpg: next trustdb check due at 2023-04-20 Success user@heads-tests:~/heads\$ make BOARD=gemu-coreboot-fbwhiptail-tpml PUBKEY ASC=~/OubesIncoming/Insurgo/Insurgo/2023 pub.asc USB TOKEN=NitrokevStorage ROOT DISK IMG=~/OubesIncoming/heads-tests/root.gcow2 OEMU MEMORY SIZE=1G run swtpm socket \ --tpmstate dir="/home/user/heads/build/x86/gemu-coreboot-fbwhiptail-tpml/vtpm" \ --flags "startup-clear" \ --terminate \ --ctrl type=unixio,path="/home/user/heads/build/x86/gemu-coreboot-fbwhiptail-tpml/vtpm/sock" & sleep 0.5 gemu-system-x86 64 -drive file="/home/user/QubesIncoming/heads-tests/root.gcow2",if=virtio \ --machine g35,accel=kvm:tcg \ -rtc base=utc \ -smp "\$(nproc)" \ -vga virtio \ -full-screen \ -m "\$(cat "/home/user/heads/build/x86/gemu-coreboot-fbwhiptail-tpml/memory")" \ -serial stdio \ --bios "/home/user/heads/build/x86/gemu-coreboot-fbwhiptail-tom1/heads-gemu-coreboot-fbwhiptail-tom1-v0.2.0-1359-g411ca09-ggg-injected.rom" \ -object rng-random.filename=/dev/urandom.id=rng0 \ -device virtio-rng-pci,rng=rng0 \ -netdev user,id=ul -device e1000,netdev=ul \ -chardev socket,id=chrtpm,path="/home/user/heads/build/x86/gemu-coreboot-fbwhiptail-tpml/vtpm/sock" \ -tpmdev emulator,id=tpm0,chardev=chrtpm \ -device tpm-tis,tpmdev=tpm0 \ -device gemu-xhci,id=usb \ -device usb-tablet \ -drive file="/home/user/heads/build/x86/gemu-coreboot-fbwhiptail-tpml/usb fd.raw".if=none.id=usb-fd-drive.format=raw \ -device usb-storage,bus=usb.0,drive=usb-fd-drive \ -device usb-host, vendorid=8352, productid=16649 \

Could not access KVM kernel module: No such file or directory gemu-system-x86 64: failed to initialize kvm: No such file or directory

File Edit View Search Terminal Help

~ # Do your tests here under Heads recovery shell!!!!

7.540900] ata4: SATA max UDMA/133 abar m4096@0xc0882000 port 0xc0882280 irq 21 7.548189] ata5: SATA max UDMA/133 abar m4096@0xc0882000 port 0xc0882300 irq 21 7.560618] ata6: SATA max UDMA/133 abar m4096@0xc0882000 port 0xc0882380 irg 21 7.593248] i8042: PNP: P5/2 Controller [PNP0303:KBD,PNP0f13:MOU] at 0x60,0x64 irq 1,12 7.6215661 serio: i8042 KBD port at 0x60.0x64 irg 1 7.634590] serio: i8042 AUX port at 0x60,0x64 irg 12 [heads-tests] OEMU 7.647184] rtc cmos 00:04: RTC can wake from 54 7.663185] input: AT Translated Set 2 keyboard as /devices/platform/i8042/serio0/input/input2 7.6902431 rtc cmos 00:04: registered as rtc0 7.702342] rtc cmos 00:04: setting system clock to 2023-02-04T10:12:45 UTC (1675505565) 7.724213] rtc cmos 00:04: alarms up to one day, y3k, 242 bytes nvram, hpet irgs 7.740848] i2c /dev entries driver 7.764239] i801 smbus 0000:00:1f.3: SMBus using PCI interrupt 7.784829] i2c i2c-0: 1/1 memory slots populated (from DMI) 7.789289] i2c i2c-0: Memory type 0x07 not supported yet, not instantiating SPD 7.814381] IR NEC protocol handler initialized 7.827132] IR RC5(x/sz) protocol handler initialized 7.838211] IR RC6 protocol handler initialized 2023-02-04 10:13:36 UTC 7.8586441 IR JVC protocol handler initialized 7.869455] IR Sony protocol handler initialized TOTP: 455670 | HOTP: N/A 7.890646] IR SANYO protocol handler initialized 7.901690] atal: SATA link down (SStatus 0 SControl 300) 7.912889] ata3: SATA link up 1.5 Gbps (SStatus 113 SControl 300) 7.919305] ata2: SATA link down (SStatus 0 SControl 300) Default boot 7.929866] ata3.00: ATAPI: QEMU DVD-ROM, 2.5+, max UDMA/100 7.934021] ata3.00: applying bridge limits 7.944067] ata4: SATA link down (SStatus 0 SControl 300) 7.948206] ata5: SATA link down (SStatus 0 SControl 300) 7.951994] ata6: SATA link down (SStatus 0 SControl 300) Refresh TOTP/HOTP 7.955687] IR Sharp protocol handler initialized 7.958866] IR MCE Keyboard/mouse protocol handler initialized 7.968067] IR XMP protocol handler initialized 7.977375] tsc: Refined TSC clocksource calibration: 2893.429 MHz 7.981025] clocksource: tsc: mask: 0xfffffffffffffffffffmax_cycles: 0x29b503c3395, max_idle_ns: 440795255992 ns 7.9913611 ata3.00: configured for UDMA/100 Options --> 8.009847] device-mapper: ioctl: 4.43.0-ioctl (2020-10-01) initialised: dm-devel@redhat.com 8.0173461 clocksource: Switched to clocksource tsc 8.028336] scsi 2:0:0:0: CD-ROM OEMU OEMU DVD-ROM 2.5+ PQ: 0 ANSI: 5 8.043865] NET: Registered protocol family 17 8.058136] IPI shorthand broadcast: enabled System Info 8,062245] sched clock: Marking stable (7999771960, 61963165)->(8089676705, -27941580) 8.112856] sr 2:0:0:0: [sr0] scsi3-mmc drive: 4x/4x cd/rw xa/form2 tray 8.117067] cdrom: Uniform CD-ROM driver Revision: 3.20 8.159959] sr 2:0:0:0: Attached scsi CD-ROM sr0 8.178569] sr 2:0:0:0: Attached scsi generic sg0 type 5 8.463339] Freeing unused kernel image (initmem) memory: 920K Power Off 8.480467] Write protecting the kernel read-only data: 14336k 8.506846] Freeing unused kernel image (text/rodata gap) memory: 2044K 8.528331] Freeing unused kernel image (rodata/data gap) memory: 1968K 8.537976] Run /init as init process 8.543498] with arguments: 8.547074] /init 8.5503111 with environment: 8.553746] HOME=/ TERM=linux 8.5571391 8.653267] [U] hello world tit enter to proceed to recovery shell: [11.558335] EXT4-fs (vdal): mounting ext2 file system using the ext4 subsystem 11.589414] EXT4-fs (vdal): mounted filesystem without journal. Opts: (null) 11.923401] random: tpm: uninitialized urandom read (20 bytes read) 11.977919] random: shred: uninitialized urandom read (312 bytes read) 11.992360] random: shred: uninitialized urandom read (312 bytes read) 43.537196] random: crng init done 43.547041] random: 7 urandom warning(s) missed due to ratelimiting !!!!! Console recovery shell New value of PCR[4]: 8a6a96fde1a8dd96271479dc40742b36aba3c2b3 !!!!! Starting recovery shell

What's Next

- TPM2 support on QEMU/KVM and SWTPM (skeleton: https://github.com/osresearch/heads/pull/1292)
- A better build system to guarantee reproducible builds based on NixOS if everything goes well (PoCs started)
- Clean room, in ram GPG key generation with backup/restore/USB thumb drive emergency usage capabilities (No more USB Security dongle strong requirement to use Heads while still highly recommended).
 - Authenticated Heads recovery shell, USB boot and more! (under design!)
- Finally: flash write protection options!
 - Platform chipset locking (only Heads can flash firmware) (PoC: https://github.com/osresearch/heads/pull/326#issuecomment-1019684512)
 - SPI Write protection, permitting to write protect coreboot's bootblock region (requires external flashing when coreboot version bumps happen under Heads. For the most paranoid only!)
 - 3mdeb dasharo/flashrom merge soon : https://github.com/osresearch/heads/pull/1251
- International keyboard support (PoC started: https://github.com/osresearch/heads/issues/555)
- On demand MAC randomization inside of Heads, overwriting GBE region inside of firmware. Persistence across firmware upgrades. (PoC started: https://github.com/osresearch/heads/pull/1195)
- Even more space for Maximized roms! (additional ~0.3-0.6mb): (PoC : https://github.com/osresearch/heads/pull/1298

References / Links

References:

Differences between linuxboot, Heads NERF Heads conference (Hudson, 33c3, 2016) Linuxboot conference (Hudson, 34c3, 2017) Heads: a call for collaboration (Laurion, FOSDEM, 2020) Coreboot measured boot, SRTM mode (coreboot doc) Heads current measured boot scheme (Heads doc)

Project homes

Heads searchable documentation Heads project's home (GitHub code/features/issues) Heads documentation's home (GitHub documentation/issues) Heads community direct link

Questions/Comments?

