

FOSDEM

Computer
Museum
NAM-IP

Understanding the Bull GAMMA 3 first generation computer through emulation

PONSARD Christophe
NAM-IP Computer Museum
CETIC Researcher, DW Champion

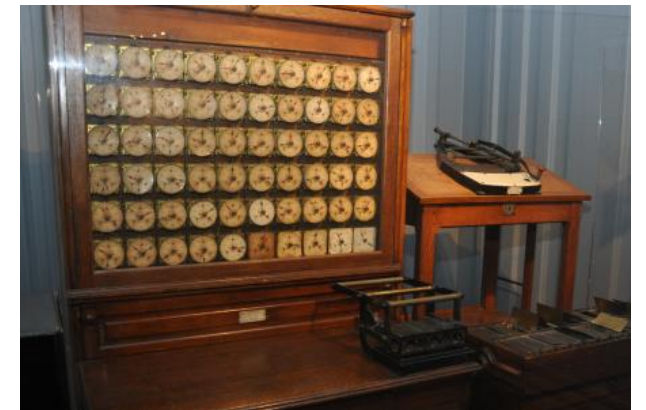
FOSDEM 23 – Emulation – February 5

Context – NAM-IP Computer Museum

Computer
Museum
NAM-IP

www.nam-ip.be

- Located in Namur/Belgium - 30' from Brussels
 - worth a visit if you are staying a few days I Belgium after FOSDEM)
 - also: Pixel Museum (in BXL) & HomeComputerMuseum (in Eindhoven/NL not so far)
- Missions:
 - Preservation: safeguarding digital heritage, focus on local pioneers
 - Acquisition of artefacts, enriching collections: Bull, Burroughs/Unysis, I&B,...
 - Exhibitions: for all, specific animation, permanent/temporary
 - **Research: about machines, software, communities → here BULL GAMMA 3**
- “Container design”, an historical parallel



Outlook

- Historical context : a long time ago...
- Discovering the machine
- A look at existing emulators
- Our JAVA emulator
- Some lessons learned

Back to Early Electronic Machines

with transition from electromechanical → electronic machines

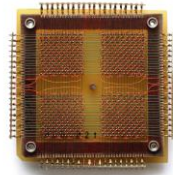
45: EDVAC & Von Neuman architecture
47: assembly (Booth, ARC)



47: bipolar transistor (exp.)



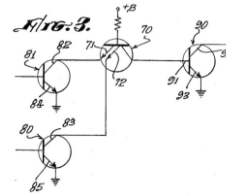
53-55: dev of magnetic core memory (Whirlwind)



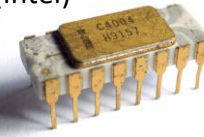
56: hard disk drive (IBM)



61: TTL



71: first μ-proc (Intel)



1945

1950

1955

1960

1965

1970

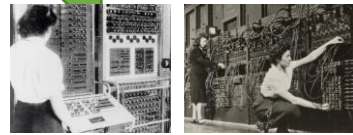


First generation
vacuum tubes, delay lines, drums

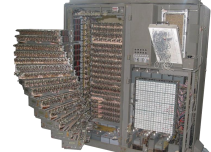
Second generation
transistors, magnetic core

Third generation
Integrated Circuits

4th gen
μProc



43: Colossus (Bletchley park) 45: ENIAC



52: GAMMA 3



53: IBM 650 "ORDINA TEUR"



55: TRADIC 59: IBM 1401



58: GAMMA 60



60: GAMMA 30 (RCA)



64: IBM 360



Electromechanical area



48: Bull BS120



49: IBM 407

Hardware (initial version)

First generation computer:

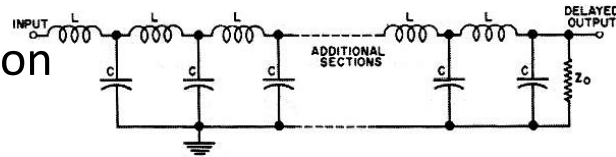
- vacuum tubes, delay lines

Code stored in a connection panel (64 instructions)

- So not Von Neumann (pgm need to be in memory)
- although somehow “memory mapped” on “serie 3”

Main memory: 7 registers

- Delay lines
- Vacuum tube for regeneration
- 8.5 kg to store... 6 bytes

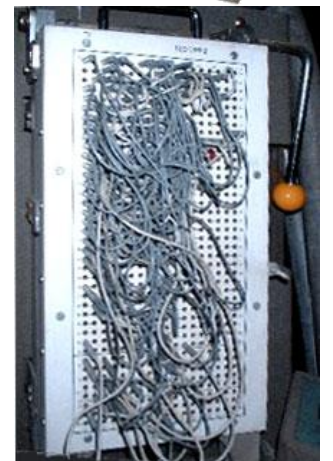
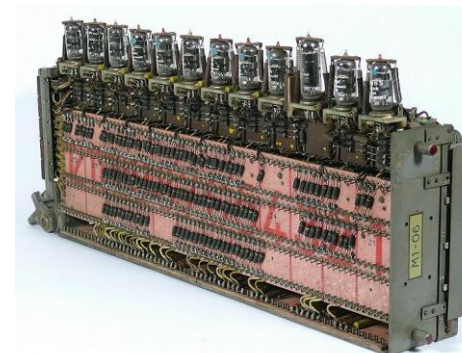
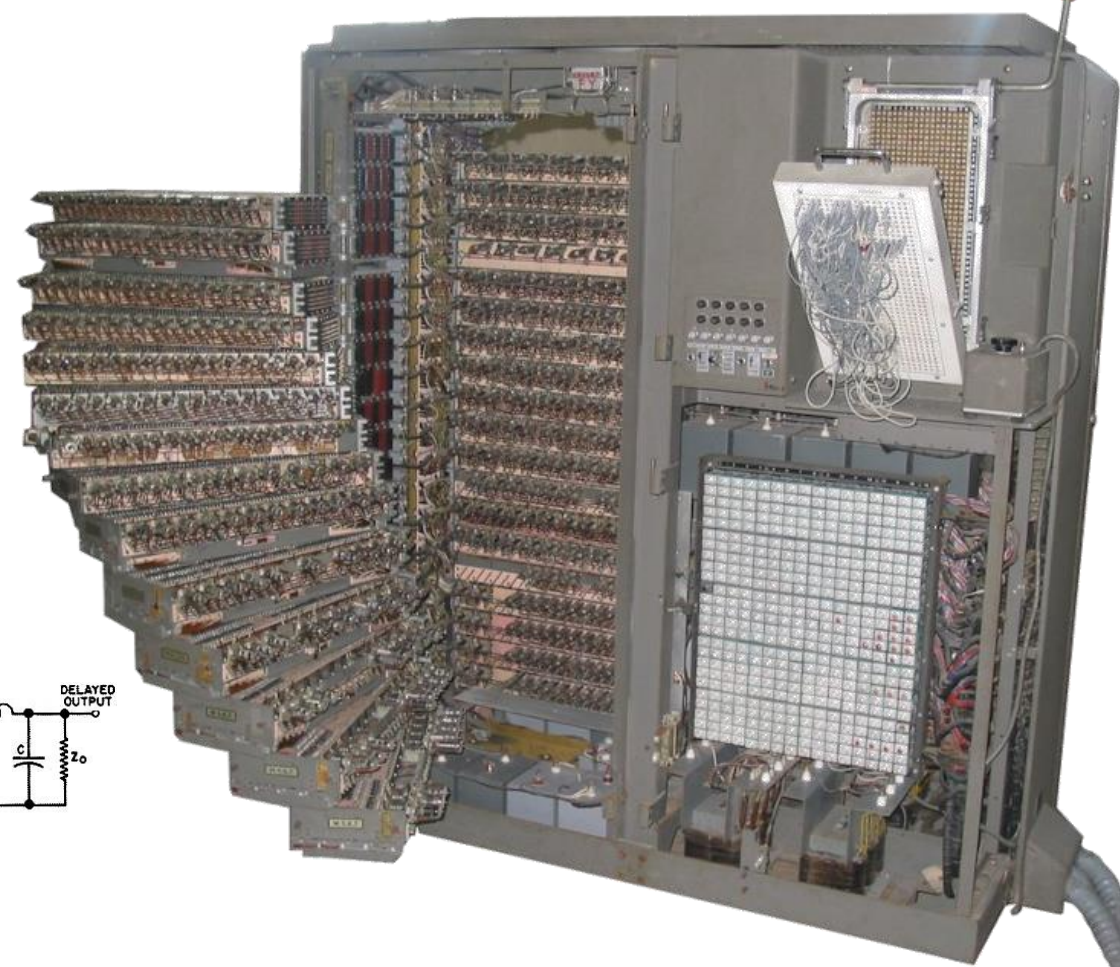


ALU:

- vacuum tubes (low reliability) → germanium diodes
- + - → * % performed internally by iterative + -

Clock: 2.5 Hz... because synchronised with motor of motor of the connected electromechanical equipment !

Nice “drawer” design easing maintenance !



GAMMA 3 : Computer and/or Calculator ?

- Initially: designed as calculator « slave » for tabulating machine

*“In its current configuration, **the computer acts as an extension unit for the punched card machine it is connected to.** The cards are read in the reader station which transmits data to the computer. The computer in turn performs all necessary calculations and transmits the results back to the punched card machine which will print or punch these values. Regardless of the task, **the computer is so fast that there is no visible delay caused by the calculations**”*

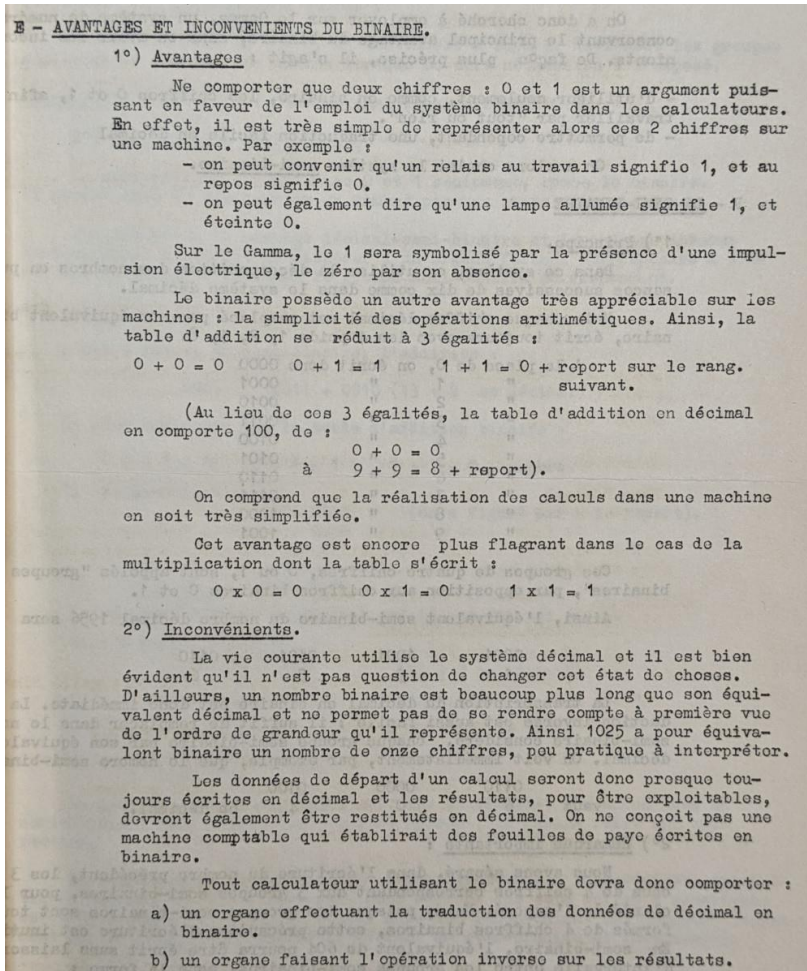
(Pierre Letort, 1953)

So programs = specific calculation routines called by tabulating machine

- Evolution:
 - 3M: scientific version with floating point support (otherwise need subroutines)
 - ET (1957): drum extension (about 100Kb) → program NOW truly in memory
- Finally: GAMMA 3 : first French computer
→ GAMMA 3 becomes the central unit
and tabulating machine becomes the peripheral device (= transition !)



Should a Computer Operate in Decimal or Binary ?



Pro:

- Only two figure: powerful
- Maps easily to relays !

Cons:

- Longuer
- Needs to translate back & forth with decimal (for humans

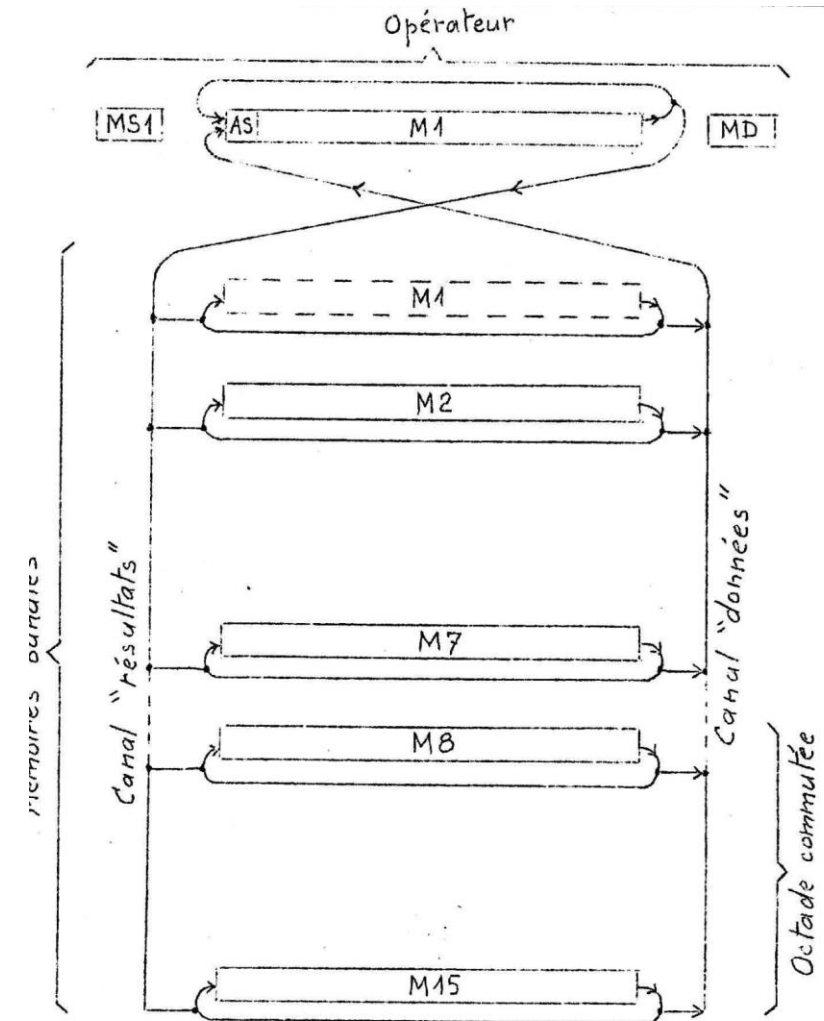
Conclusion ?

- Use « semi-decimal » → binary-coded decimal !
- Char = 4 bit to code 1 BCD (or 1 HEX)

Note: later extension (ET) supported full binary mode

GAMMA 3 – Core “banale” memory : a few registers

- M0=M1: “accumulator” IN/OUT
- M2..M7: only IN
- M8..M15: “extra memory”
(switched “octade” see extension)
- Technology: delay line
 - 1 register = 1 word
 - 1 word of 12 chars
of 4 bits (BCD or HEX)
 - ➔ 1 word = 6 bytes (note: longer than 32bit integer)
 - ➔ M1..M7 = 42 bytes of “main” memory

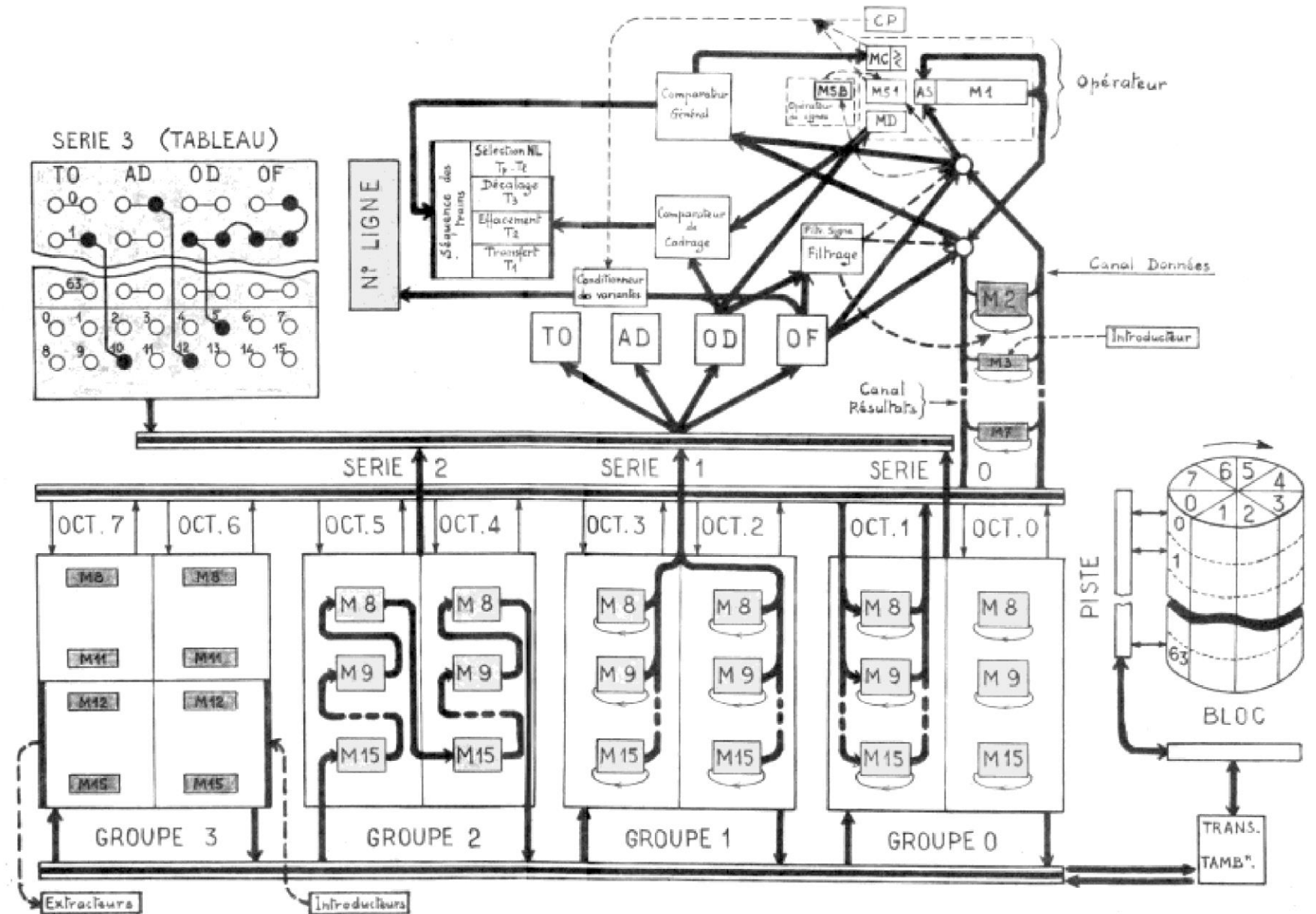


GAMMA 3 Complete Architecture

Note also:

PC, CMP, shift...

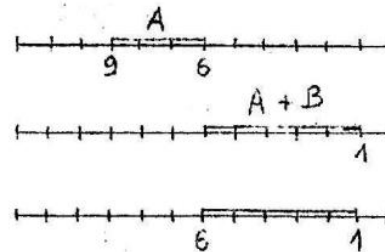
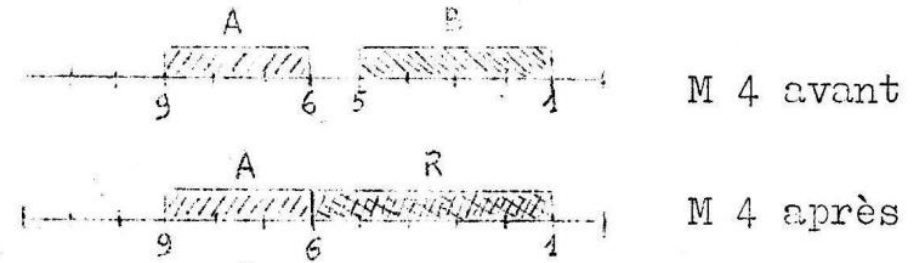
Used in instructions



GAMMA 3 – Structure of Instruction Set

Structure: 4 chars

- TO: Type of Operation
 - E.g. AN (add), SN (subtract), MR (reduced mult.), MC (complete mult.),...
- AD: ADdress
 - Memory register involved in operation
- OD: Ordre Début (i.e. start position)
- OF: Ordre Fin (i.e. end position)
 - Because operation can consider a subrange of a Word !



TO	AD	OD	OF		MD
6	4	6	9	BO	6
10	4	1	5	AN	1
8	4	1	6	OB	1

1 Word = 12 char

➔ so possible to store 3 instructions/word

Coding sheets

Problème: $B = \frac{UB \cdot 10^3}{\sqrt{V} \sqrt{3}}$

B tjs compris entre 1 et 1,99

Sélection lignes	NL	Codes				Opération	MD	M1				M2				M3				M4			
		TO	AD	OD	OF			0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1
		Carte N° X																					
		4	2	7	1	KB	0																
	9	4	2	6	7	KB	0																
		4	2	5	3	KB	0																
		4	2	4	2	KB	0																
		6	3		4	BO	0																
	8																						
		12	2	4	8	MR	0																
		6	1	2		BO	2																
	7	8	2	4	10	OB	4																
		6	4	3	6	BO	3																
		9	2	6	9	CN	6																
	6																						
		13	2	4	10	DR	0																
		6	1		3	BO	0																

Handwritten notes on the table:

- Under M1: \sqrt{V} , $Y \cdot \sqrt{3}$
- Under M2: $\sqrt{3}$, $Y \cdot \sqrt{3}$
- Under M3: UB , $UB \cdot 10^3$, Multiplication par 10^3
- Under M4: \sqrt{VB}
- At the bottom: B décimales

Exploring the Instruction Set (excerpt)

0XXX – V variant

- 0000 NOP ;-)
- 01xx JUMP if > flag set
- 02xx JUMP if = flag set
- 03xx JUMP if >= flag set
- 04xx JUMP if neg flag set

1XXX

- 11/12/13xx: VCS: serie switch
- 15/16/17xx: VRS: serie return
- 18/19xx: out to connected machine
- 1Axx: decimal mode (CD)
- 1Fxx: binary mode (CB)
- ...

2XXX – drum transfer (BT)

...

3XXX – setting memory to zero

4XXX – constant transfer to memory

- AD: mem
- OD: position
- OF: value

5XX – copy between octads

6XX – transfer to operative mem. (BO)

- AD: mem
- OD & OF: range

7XX

- 71/72xx: shift memory
- 73/74xx: logical AND (what about OR ?)

8XX – transfer from operative mem. (OB) Note:

- AD: mem
- OD & OF: range

9XXX – comparison operations

...

AXXX – addition (AN)

BXXX – subtraction (SN)

CXXX – reduced multiplication (RM)

DXXX – reduced division (RD)

EXXX – full multiplication (MC)

FXXX – full division (FD)

- “reduced” * / for “small numbers”
➔ range need to be manager
- “complete” * / operation using M1+M2 as extended storage

A look at the code card

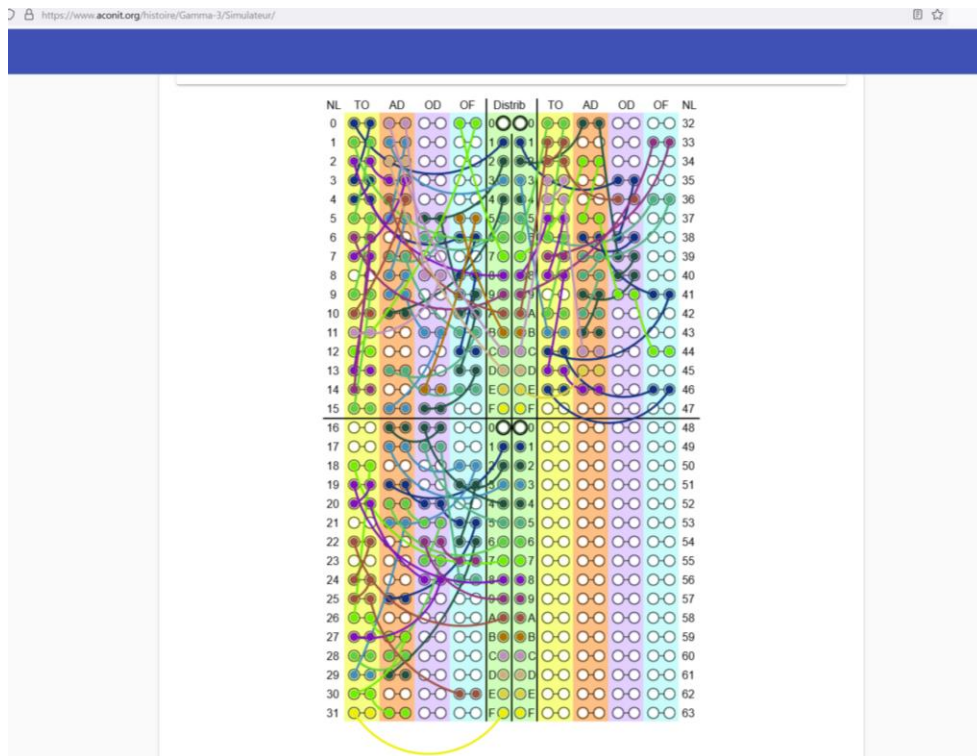
- “ordonnateur”
“ordinateur” in French was coined 1 year later by J. Perret for IBM
“god puts world in order”
- Not so easy to decode...

TABLEAU DE CODE - E.T. ORDONNATEUR

															M V P X R Z																										
															Série	OF	AD →			0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	← AD					
															0	0	4	8	12	Jamais	>	=	≥	M _S							9,14	15,14	1,14	0,14	11,14	12,14	13,14	14,14			
															1	1	5	9	13	Toujours	≤	≠	<	M _S [†]							8,3	9,3	0,3	11,3	12,3	13,3	14,3	15,3	0 V		
															2	2	6	10	14	V ₀	V ₁	V ₂	V ₃	V ₄	V ₅	V ₆	V ₇				Vac ₀	Vac ₁	Vac ₂	Vac ₃	Vac ₄	Vac ₅	Vac ₆				
TT → TO = 2															3	3	7	11	15	V ₀	V ₁	V ₂	V ₃	V ₄	V ₅	V ₆	V ₇				Vac ₀	Vac ₁	Vac ₂	Vac ₃	Vac ₄	Vac ₅	Vac ₆				
															0	0	0	0	1	2	3	VCS _{B10}	VCS _{RNL1}	VCS _{RNL2}				VRS _{RNL1}	VRS _{RNL2}	VRS _{VMC}	ES ₁	ES ₂	CD				CO _(OF)	CS _Z _(OF)	CB	1	
															1	2	1	1	4	5	6	7	AMD _(OF)	BD							IL _{OF}				IL _{M2}	7					
															2	4	2	2	8	9	10	11																			
															3	6	3	3	12	13	14	15	0			1			2			3 à 15			← AD						
															4	8	4	4	16	17	18	19	R à Z			R à Z			Maintien MD			3 ZB									
															5	10	5	5	20	21	22	23	Emission 48 v.			Filtrée OF → M ₁			Filtrée OF → MB			4 KB									
															6	12	6	6	24	25	26	27	OD = Em OF → Récepteur									5 GG									
															7	14	7	7	28	29	30	31	OF → M ₁			Maintien f. M ₁			MB → M ₁			6 BO									
															0	1	8	8	32	33	34	35	R à Z f. M ₁			R à Z f. MB			opérations			8 OB									
															1	3	9	9	36	37	38	39	Suppres cadrage préalable			M ₁ ≥ OF			M ₁ ≤ MB			9 CN									
															2	5	10	10	40	41	42	43	M ₁ + OF			M ₁ × 2			M ₁ + MB			10 AN									
															3	7	11	11	44	45	46	47	Maintien MD			M ₁ - OF			R à Z f. M ₁			M ₁ - MB			11 SN						
															4	9	12	12	48	49	50	51	M ₁ × OF = M ₁						M ₁ × MB = M ₁			12 MR									
															5	11	13	13	52	53	54	55	M ₁ ÷ OF = M ₁						M ₁ ÷ MB = M ₁			13 DR									
															6	13	14	14	56	57	58	59	M ₂ × OF = M ₁ M ₂						M ₂ × MB = M ₁ M ₂			14 MC									
															7	15	15	15	60	61	62	63	M ₁ M ₂ ÷ OF = M ₂						M ₁ M ₂ ÷ MB = M ₂			15 DC									
															Grou	AD	Piste	OD	Bloc			OF	OD	NL			0			1			2			3 à 15			← AD		TO

BullGammator (ACONIT, 2020)

- Open Source : <https://github.com/lutrampal/bullgammator>
- Web-base written in Javascript (nodeJS), very well documented
- Include panel interface, library, console (with stepping mode “titiller”)
- Nice code base for emulator core with unit tests
→ analysed and mainly transposed in JAVA

A screenshot of the BullGammator web interface showing the execution and control panels. The browser address bar shows "https://www.aconit.org/histoire/Gamma-3/Simulateur/".

Execution

```
Programme "Equation 2nd Degre Deca" chargé
Programme "Racine Carree" chargé
```

Titiller **Continuer**

Controle

NL	C0	M0/M1	000000000000
MS1	0	M2	000000000000
MD	0	M3	014400000052
MCMP	<	M4	000000000050
RNL1	00	M5	000000000000
RNL2	00	M6	000000000000
Octade	0	M7	000000000000
Seizaine	0	Mode	Décimal

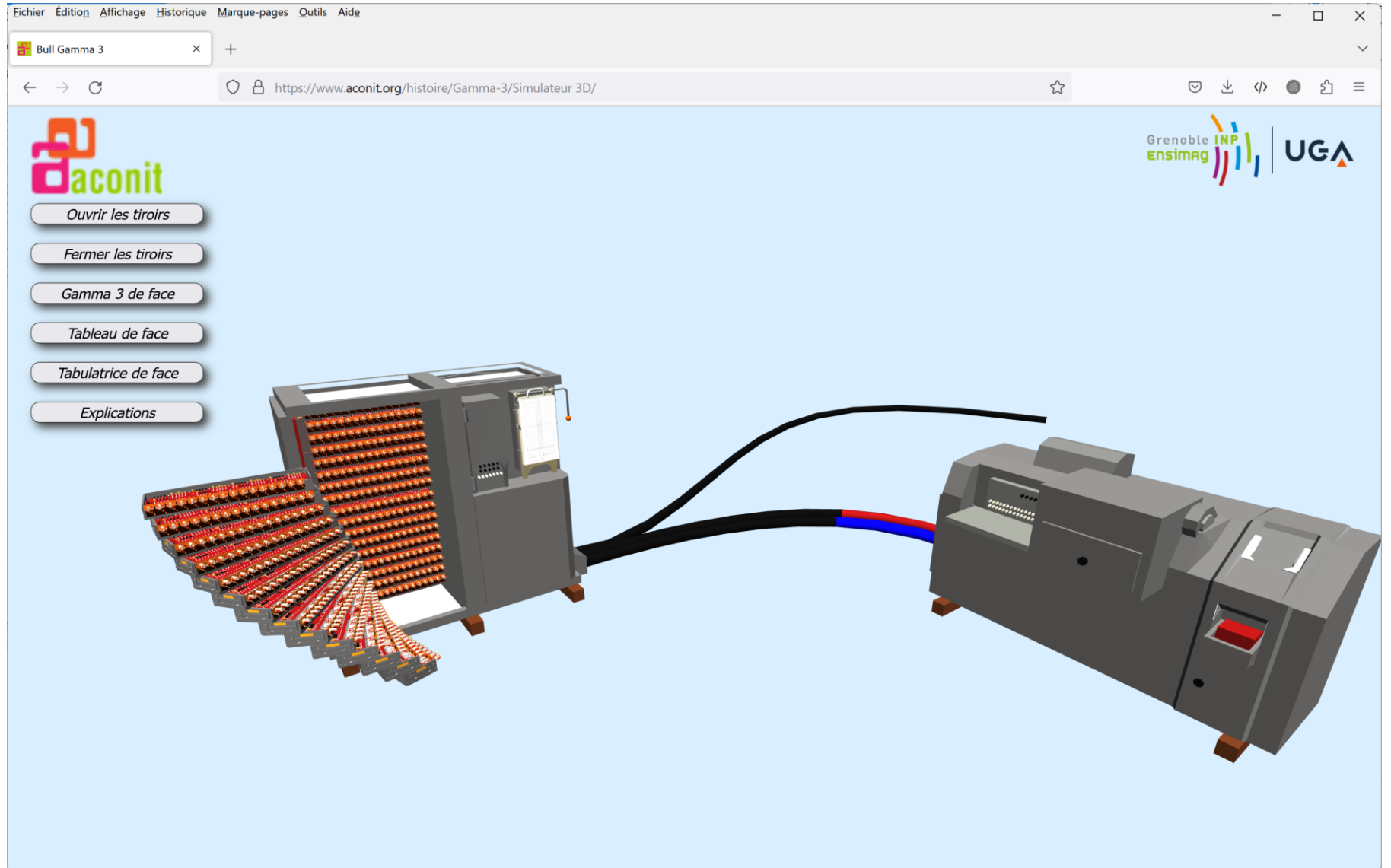
Octade 0

M8	000000000000
M9	000000000000
M10	000000000000
M11	000000000000
M12	000000000000
M13	000000000000
M14	000000000000
M15	000000000000

Série 3

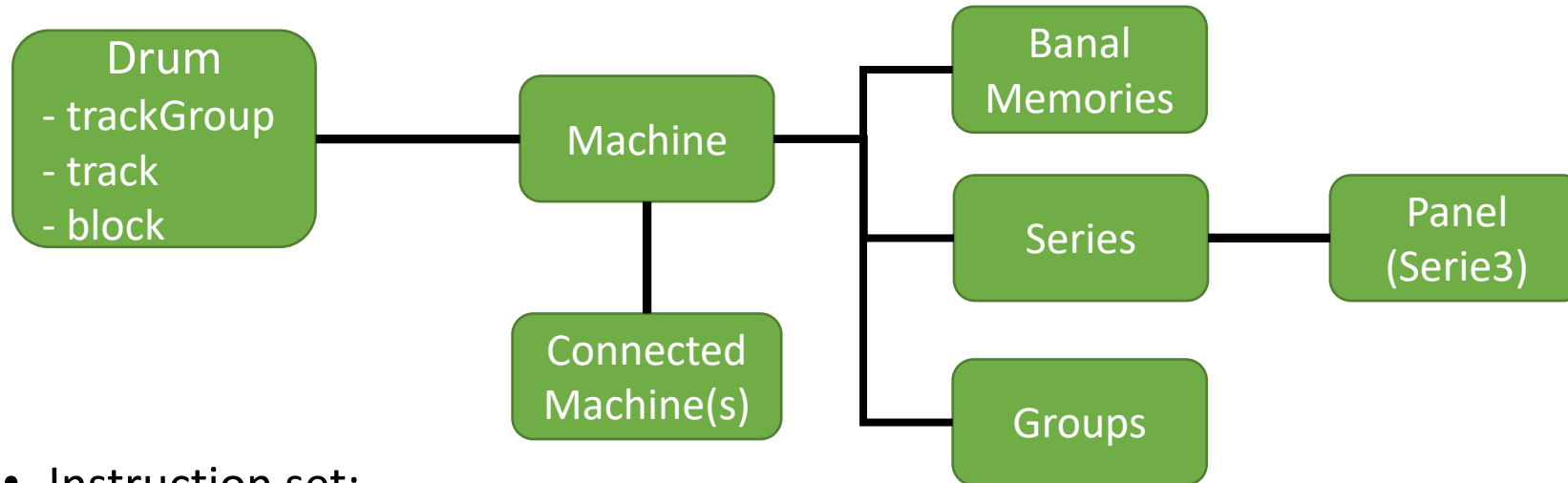
0	1c07 -- CO	32	6200 -- BO
1	6300 -- BO	33	a009 -- AN
2	8d00 -- OB	34	a700 -- AN
3	1800 -- ES1	35	c010 -- MR
4	1a00 -- CD	36	c0a5 -- MR
5	632b -- BO	37	8700 -- OB
6	9051 -- CN	38	6110 -- BO
7	8500 -- OB	39	9520 -- CN
8	03c1 -- V	40	8520 -- OB
9	6302 -- BO	41	0271 -- V
10	a402 -- AN	42	6500 -- BO
11	c035 -- MR	43	3200 -- ZB
12	7001 -- AMD	44	1c07 -- CO
13	8502 -- OB	45	8e00 -- OB
14	90b5 -- CN	46	1801 -- ES1
15	6320 -- BO	47	0000 -- V
16	0440 -- V	48	0000 -- V
17	0350 -- V	49	0000 -- V
18	7003 -- AMD	50	0000 -- V
19	8102 -- OB	51	0000 -- V
20	8610 -- OB	52	0000 -- V
21	0361 -- V	53	0000 -- V
22	a092 -- AN	54	0000 -- V
23	0069 -- V	55	0000 -- V
24	a085 -- AN	56	0000 -- V
25	a100 -- AN	57	0000 -- V
26	7000 -- AMD	58	0000 -- V
27	8700 -- OB	59	0000 -- V
28	6600 -- BO	60	0000 -- V
29	3200 -- ZB	61	0000 -- V
30	700a -- AMD	62	0000 -- V
31	f700 -- DC	63	0000 -- V

3D Simulator (by Aconit)

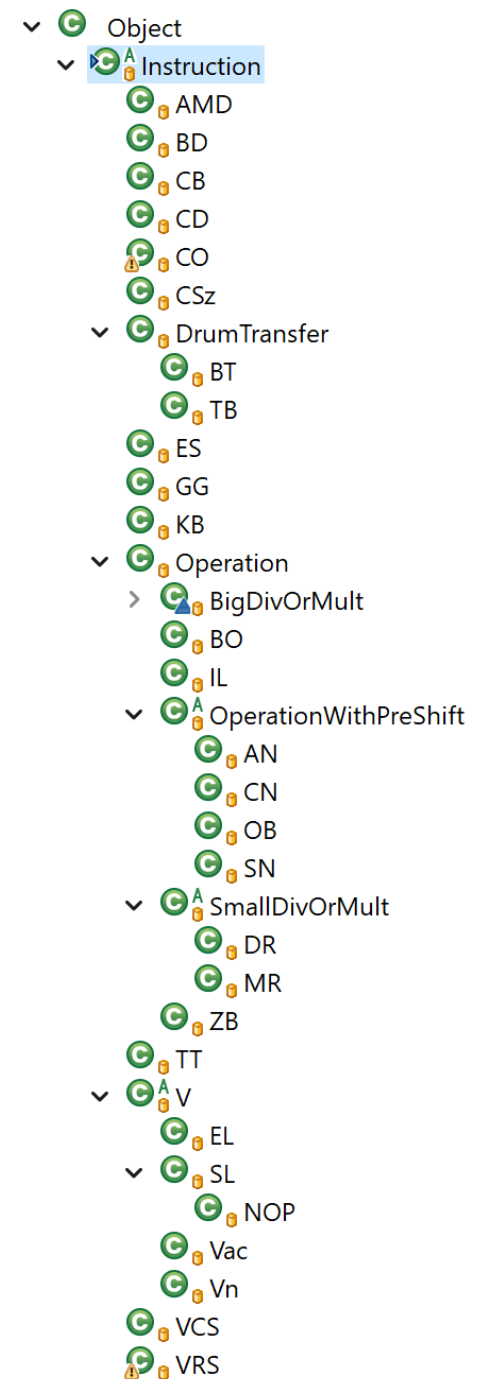


Java(script) Emulator Structure

- Machine description (see structure)
 - BullGamma → memory, series, drum, connected machines,...



- Instruction set:
 - Execute method taking instruction and machine (context) as parameter
 - By type, using a object hierarchy
- Execution management, UI (basic)
- Tests (w.r.t. spec)
- Code: <https://github.com/NAMIP-Computer-Museum/gamma3>



A look at the addition – carry algo (dec/bin fine)

```
/**
add the given memory to this one - this works both in decimal and binary mode
@param other the memory that should be added
@param from index of the block from which the addition should start
@param to index of the block to which the addition should end (excluded)
@param overriding_carry if true, at the end of the addition, the resulting carry out will override the next
memory block if it is not null. Otherwise it will be added to the next memory block.
*/
public void add(Memory other, byte from, byte to, boolean overriding_carry) {
    assert (from >= 0) : "from should not be negative";
    assert (from < to): "from should be inferior to "+ to;
    assert (to <= this.blocks.length) : "to should be inferior to the number of blocks per memory";
    int carry = 0; // implementation simulates the real work across memories
    for (int i = from; (i < to) || ((carry == 1) && !overriding_carry); i++) { // TODO check precedence
        int other_val = i < to ? other.blocks[i] : 0;
        int res = this.blocks[i%this.blocks.length + (this.blocks.length - NB_CHRS_PER_WORD)] + other_val + carry;
        if (res >= this.getMode().base) {
            carry = 1;
            res -= this.getMode().base;
        } else {
            carry = 0;
        }
        this.blocks[i%this.blocks.length + (this.blocks.length - NB_CHRS_PER_WORD)] = (byte)res;
    }
    if (overriding_carry && (carry!=0)) {
        this.blocks[to%this.blocks.length] += carry;
    }
}
```

A look at the subtraction → Java subtract !

```
/**
subtract the given memory to this one
@param other the memory that should be subtracted
@param from index of the block from which the subtraction should start
@param to index of the block to which the subtraction should end (excluded)
 */
public void subtract(Memory other, byte from, byte to, byte this_from, byte this_to) {
    assert (from >= 0) : "from should not be negative";
    assert (from < to) : "from should be inferior to to";
    assert (to <= this.blocks.length) : "to should be inferior to the number of blocks
per memory";
    long valM1 = this.getDecimalValue(this_from, this_to) - other.getDecimalValue(from,
to); // implem is done through translation to java long (int not enough !)
    this.setDecimalValue(Math.abs(valM1), this_from, this_to);
    if (valM1 < 0 && this.getMode() == MemoryMode.DECIMAL) {
        this.bullGamma.ms1 = 10;
    }
}
```

A look at the division

```
/**
divide the given memory to this one
@param other the memory that should be divided
@param from index of the block from which the division should start
@param to index of the block to which the division should end (excluded)
 */
public void divide(Memory other, byte from, byte to) {
    long vmb = other.getDecimalValue(from, to);
    if (vmb == 0) {
        throw new Error("Division by 0.");
    }
    while (this.bullGamma.md > 0) {
        while (this.getDecimalValue((byte) (from + this.blocks.length - NB_CHRS_PER_WORD), (byte) this.blocks.length) <
vmb && this.bullGamma.md > 0) {
            this.shiftLeft();
            this.bullGamma.md--;
        }
        while (this.getDecimalValue((byte) (from + this.blocks.length - NB_CHRS_PER_WORD), (byte) this.blocks.length)
>= vmb) {
            this.blocks[0]++;
            this.subtract(other, from, to, (byte) (from + this.blocks.length - NB_CHRS_PER_WORD),
(byte) this.blocks.length);
        }
    }
}
```

Current Experimentation Environment

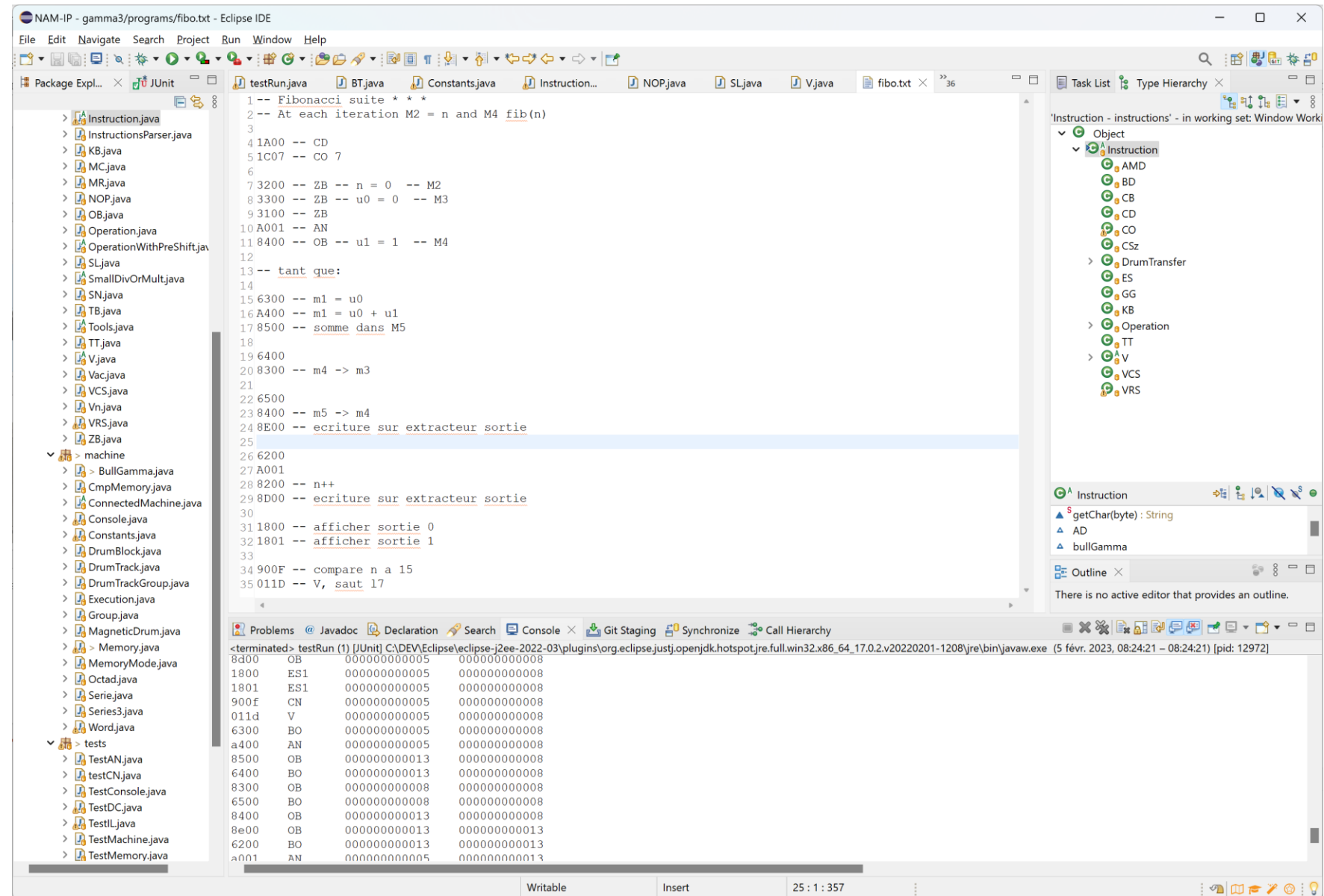
IDE (Eclipse)

Fibonacci suite

Utilities:

- sqrt

- 2nd degree



Some available FP subroutines

AVANT L'OPÉRATION							APRÈS L'OPÉRATION					
Opération	Durée opération	Variante	M ₁	M ₄	M ₅	M ₆	M ₇	M ₁	M ₄	M ₅	M ₆	M ₇
AB	1 pt	0 0 2 5			A	B		AB	inchangée	A	AB	inchangée
AB - C	2 pt	0 0 2 9			A	B	C	AB - C	inchangée	A	AB	AB - C
AB + C	2 pt	0 0 2 13			A	B	C	AB + C	inchangée	A	AB	AB + C
A + B	1 pt	0 0 7 13				A	B	A + B	inchangée	inchangée	A	A + B
A - B	1 pt	0 0 7 1				A	B	A - B	inchangée	inchangée	A	A - B
A / B	1 pt	0 0 15 13			x	A	B	A / B	inchangée	inchangée	A	A / B
Ax ² + Bx + C (CD av. 4 col. 6) sin. et cos. x ..	4 pt 4 cartes	0 0 2 13 en ligne 11		C	inchangée		B	Ax ² + Bx + C	o chang.	x inchangée	o sin. x	o cos. x
arctg N/D	4 cartes				inchangée	N	D	arctg	chang.	inchangée	chang.	arctg

Fig. 197

N.B. — Les sous-programmes Sin. cos. x et arctg N/D altèrent la mémoire 3.

Lessons Learned...

- Exploring early machines quite a strange journey:
 - Emerging concepts, still being explored
 - Gone technologies
 - ➔ need to look at larger historical context
- Not so complex to code
but many details
and limited original reference to test against
- Still a lot to explore (e.g. floating point), implement (UI...), gather/experiment
with “code”
- In summary, an very rewarding experience both technically and culturally !

Questions ?

Some reference & credits

- Fédération des Equipes Bull for access to their GAMMA3 documentation at NAM-IP museum or online: <http://www.feb-patrimoine.com/projet/gamma3/gamma3.htm>
- Vincent Joguín for its DOS-based emulator
 - vidéo (french): https://www.youtube.com/watch?v=X_ermLbQYLI
 - executable: <http://vincent.joguin.com/GAMMAET.ZIP>
 - José Maillard and Lucas Trampal for the Open Source javascript emulator (coordinated by ACONIT) <https://github.com/lutrampal/bullgammator/>
- ACONIT for on-line documentation and running emulator and keeping the GAMMA3 memory alive with students
 - documentation: <https://www.aconit.org/histoire/Gamma-3>
 - online emulator: <https://www.aconit.org/histoire/Gamma-3/Simulateur>
- Code: <https://github.com/NAMIP-Computer-Museum/gamma3>

Visit us: www.nam-ip.be Twitter @ComputerMuseumB

Contact me: christophe.ponsard@cetic.be @cponsard @cponsard@ludosphere.fr