



FreeRTOS on RISC-V

...and Open Source @ Amazon

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Agenda

The FreeRTOS Kernel

Open Source at Amazon

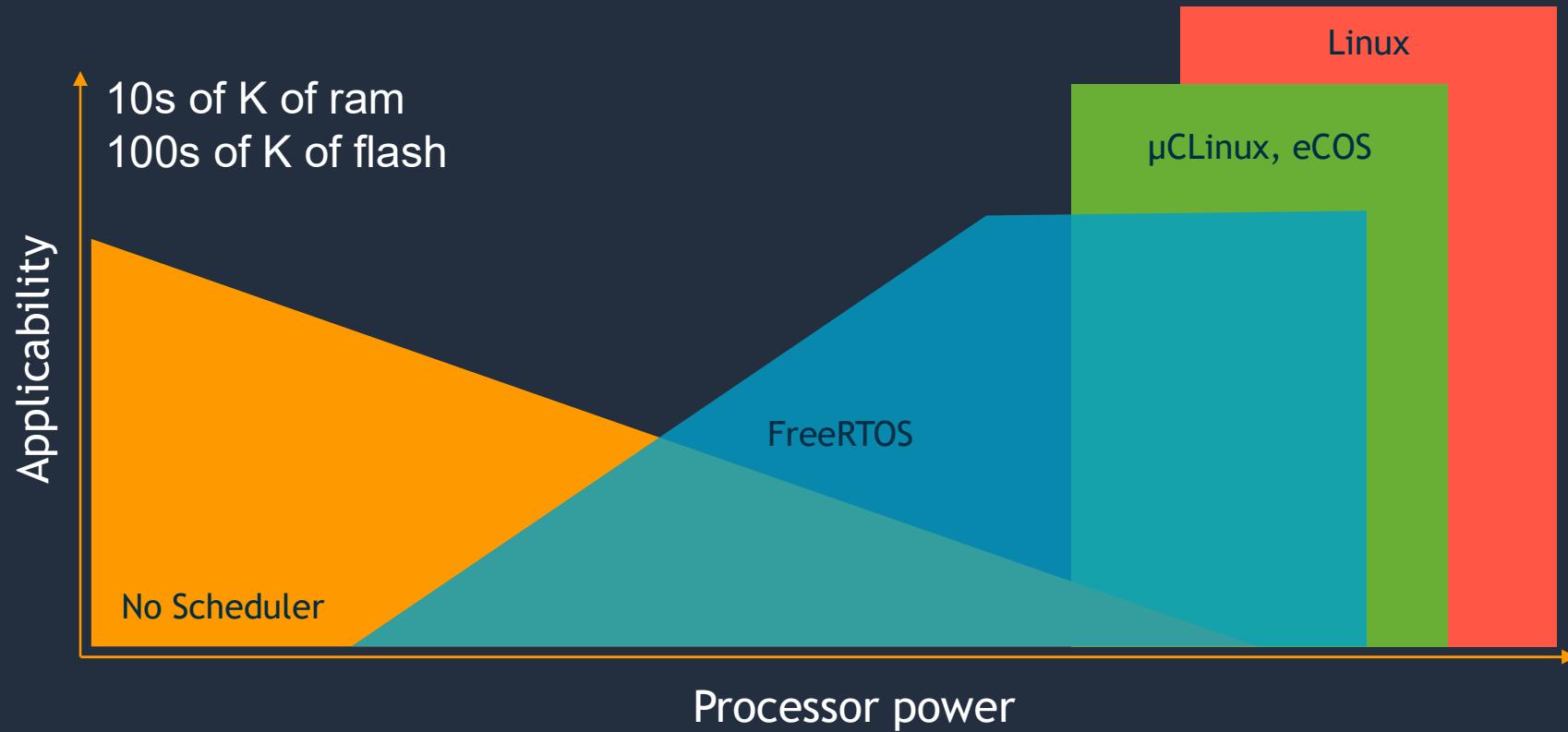
Running FreeRTOS on RISC-V

FreeRTOS—Open source real time kernel



is everywhere...

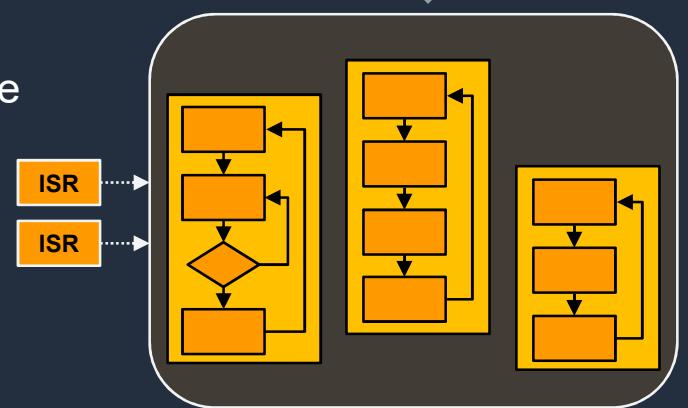
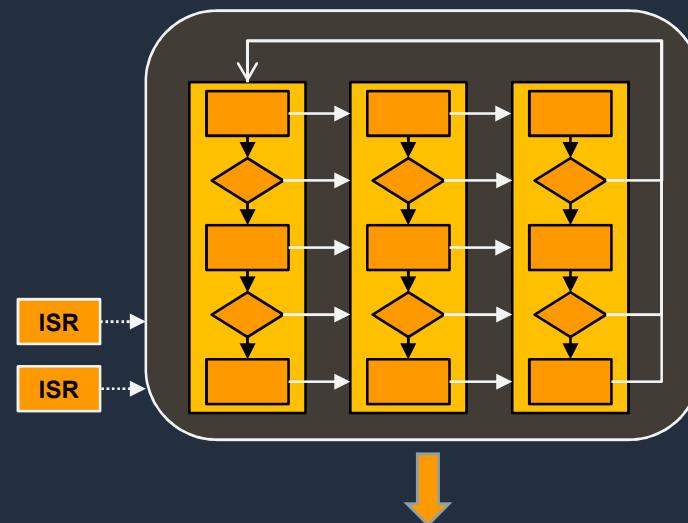
Small footprint



Library that implements multithreading

Application Design Goals:

- Meet real time requirements!
- Maximize responsiveness
- Use as little CPU/Power as possible
- Maximize maintainability
- Maximize portability (hardware change)
- Simplicity!
- Fast to market
- Meet requirements with minimum expenditure



Provide a free product that
surpasses the quality and service
of commercial alternatives.

Enterprise
friendly licensing

Robustness

Leadership

Paid options

User obsession

Documentation

Knowledgeable
support

Demonstrable
code quality

Rapid support

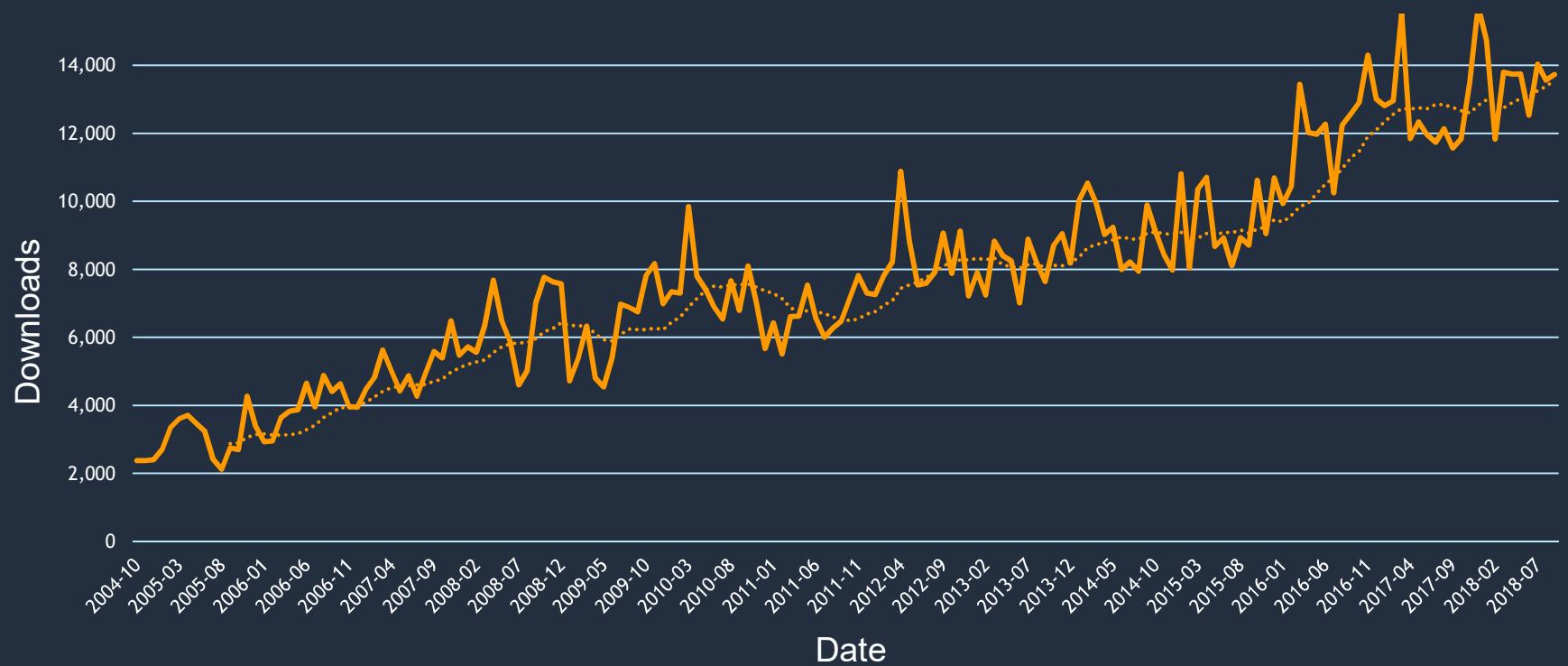
Windows hosts
too

Controlled IP

Vibrant activity



FreeRTOS downloads per month over 15 years



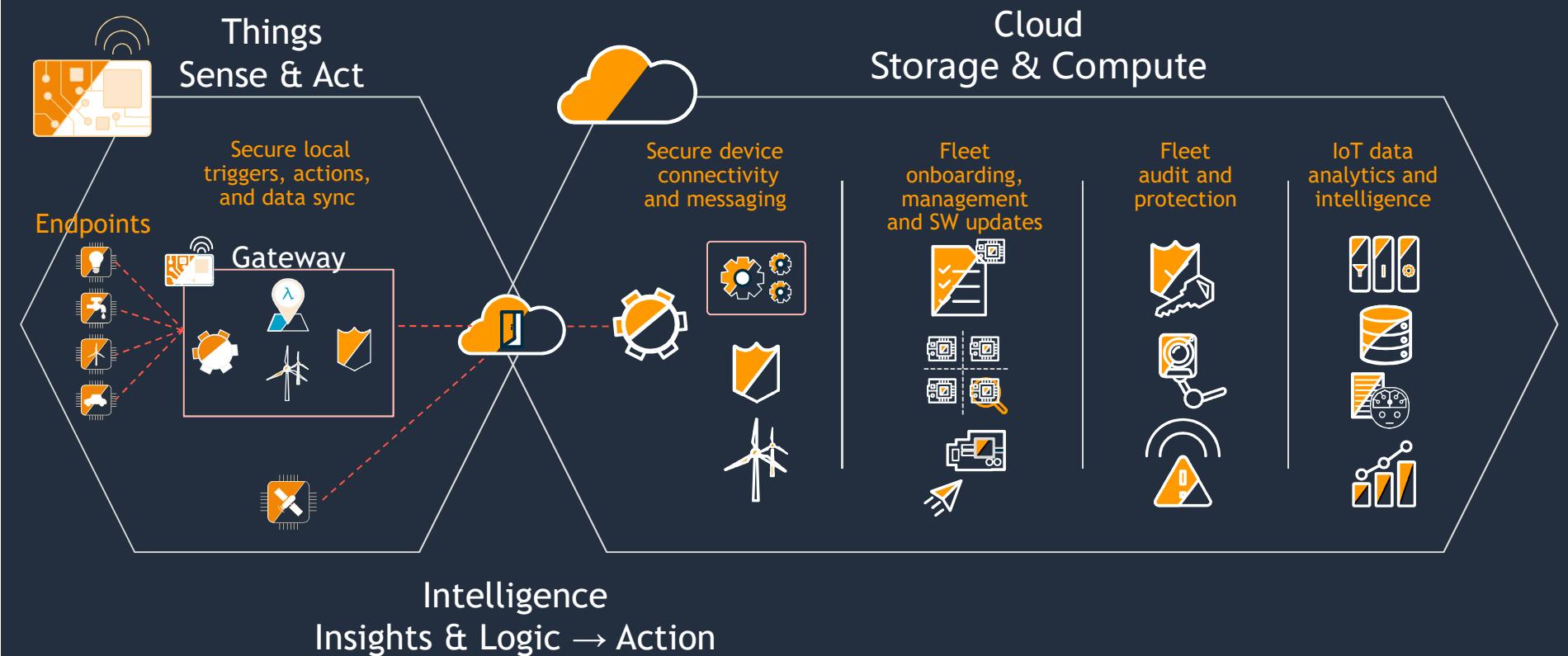
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Use Cases



AWS in the cloud and at the edge



Open Source @ Amazon

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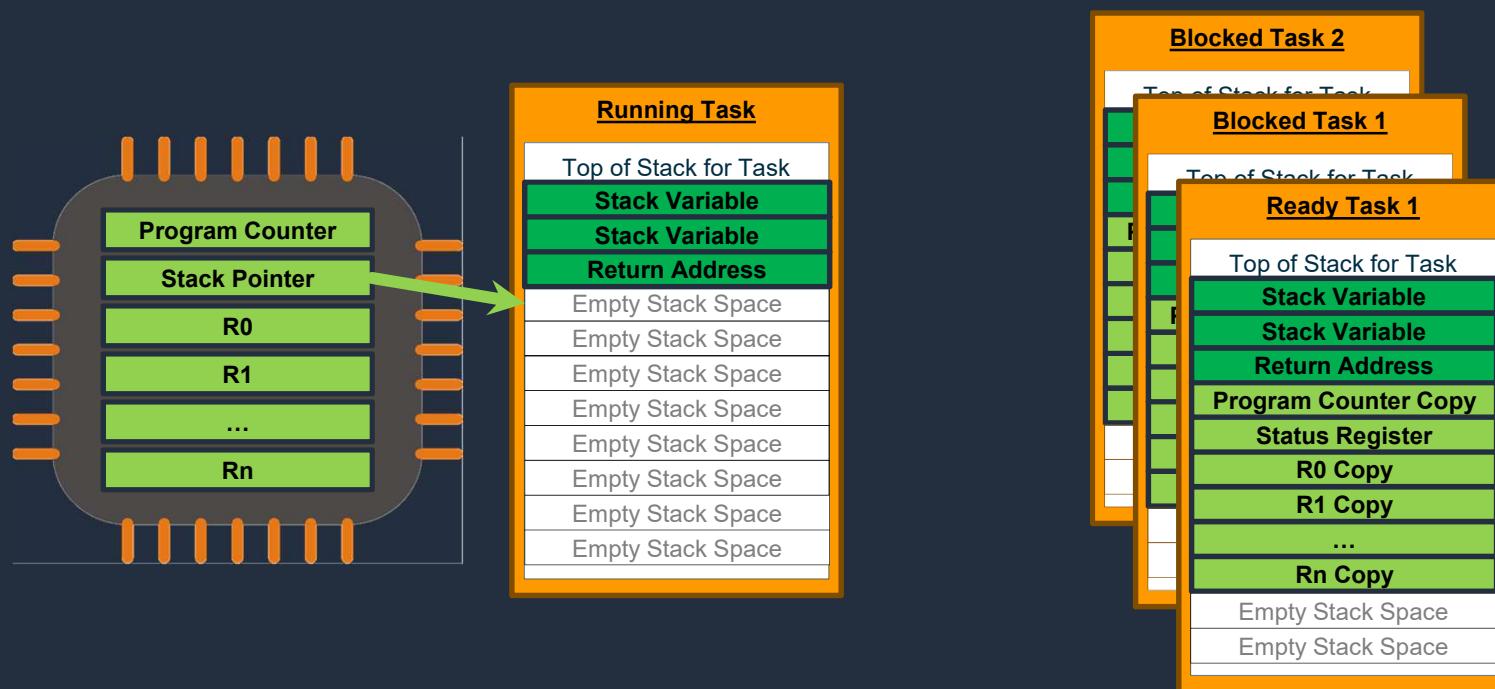


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Common source files and a port layer



MIT Licensed C Source Code

The screenshot shows a Command Prompt window with the title "Command Prompt". The window displays a file tree structure:

```
C:.
├── event_groups.c
├── list.c
├── queue.c
├── stream_buffer.c
├── tasks.c
└── timers.c

└── include
    └── portable
        └── GCC
            ├── ARM_CM0
            │   ├── port.c
            │   └── portmacro.h
            ├── ARM_CM3
            │   ├── port.c
            │   └── portmacro.h
            └── ARM_CM3_MPU
                ├── port.c
                └── portmacro.h
```

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MIT Licensed C Source Code

```
event_groups.c
list.c
queue.c
stream_buffer.c
tasks.c
timers.c

include
portable
└── GCC
    └── RISC-V-RV32
        ├── port.c
        ├── portASM.S
        └── portmacro.h

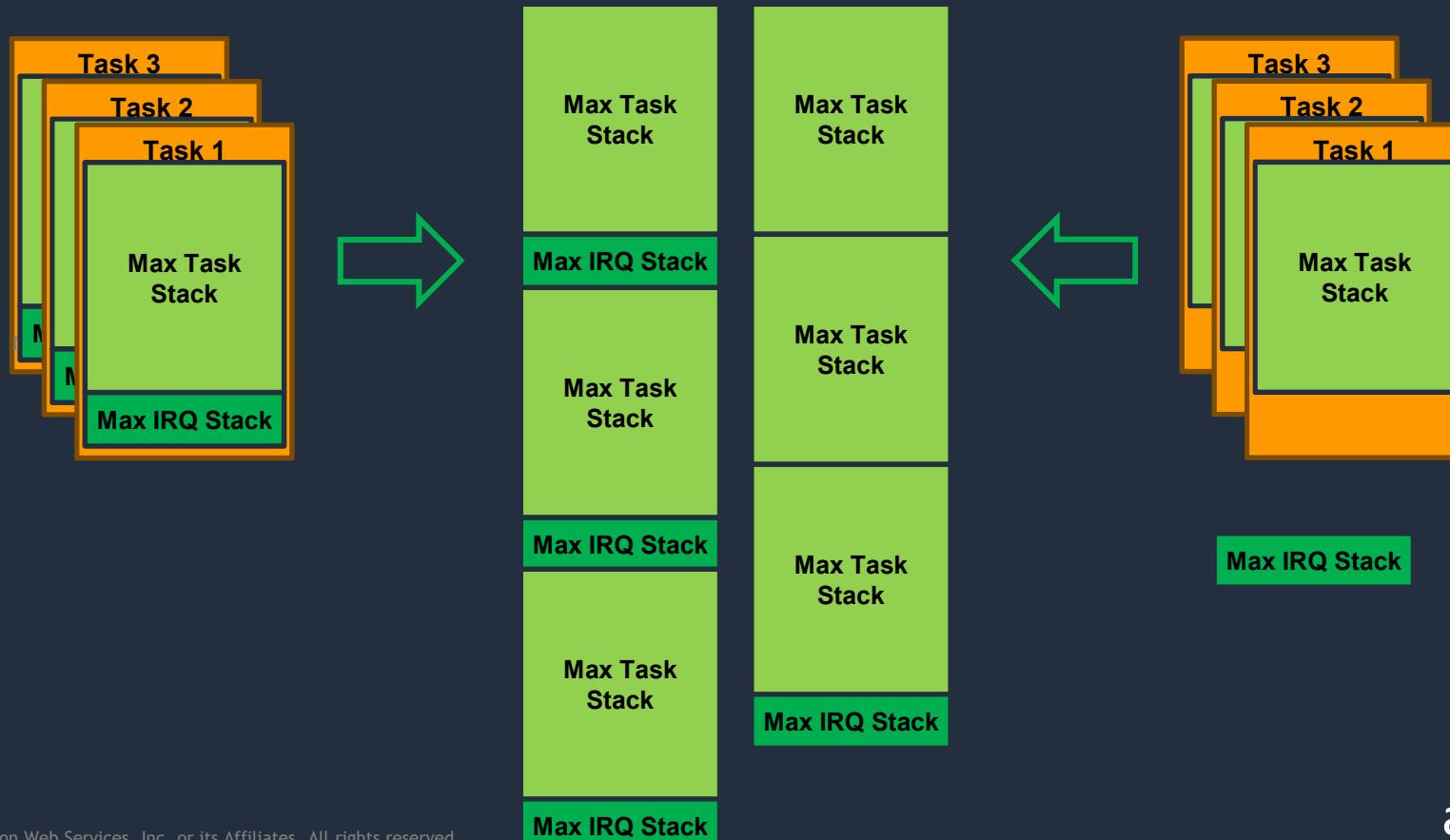
        └── chip_specific_extensions
            ├── Pulpino_Vega_RV32M1RM
            │   └── freertos_risc_v_chip_specific_extensions.h

            └── RV32I_CLINT_no_extensions
                └── freertos_risc_v_chip_specific_extensions.h
```

Demonstrate Renode and Vegaboard projects

- Including the source files
- Setting the assembler's include file
- Set configCLINT_BASE_ADDRESS
- #define portasmHANDLE_INTERRUPT
- Install the FreeRTOS trap handler

Defining the interrupt stack



Thank You!

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