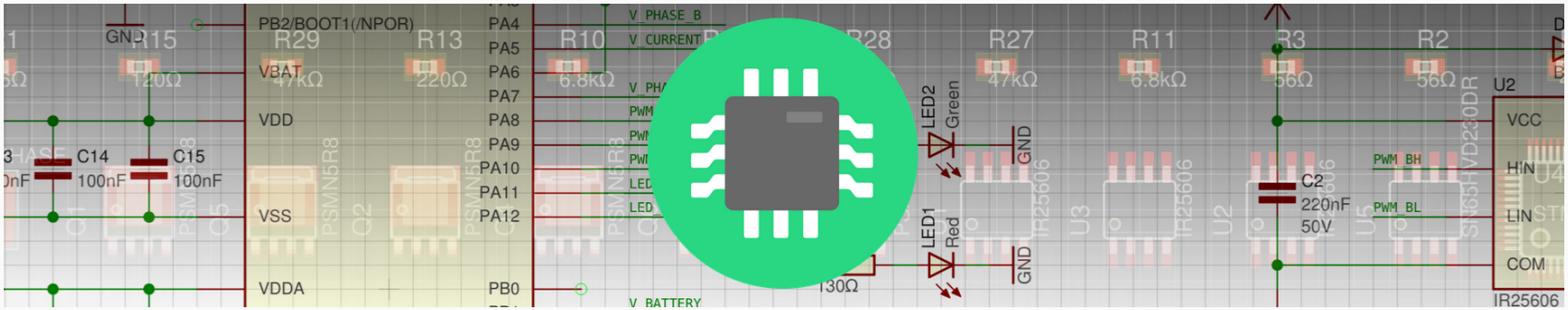
LibrePCB

A new, powerful and intuitive EDA tool for everyone

Urban Bruhin

February 3, 2018

About LibrePCB



Free/OpenSource EDA Suite

- Multiplatform
- Written from scratch in C++11/Qt5
- Development started in 2013
- Website: `http://librepcb.org/`
- GitHub: `https://github.com/LibrePCB/LibrePCB`

Motivation

Frustration about existing EDA tools

- Library system
- File format
- Usability

Library Management

Problem

- Different library types (e.g. *.lib, *.pretty, *.3dshapes)
- Tools do not (completely) handle library management
  - No integrated tool to install and update libraries
  - No dependency management
  - Complicated project library management

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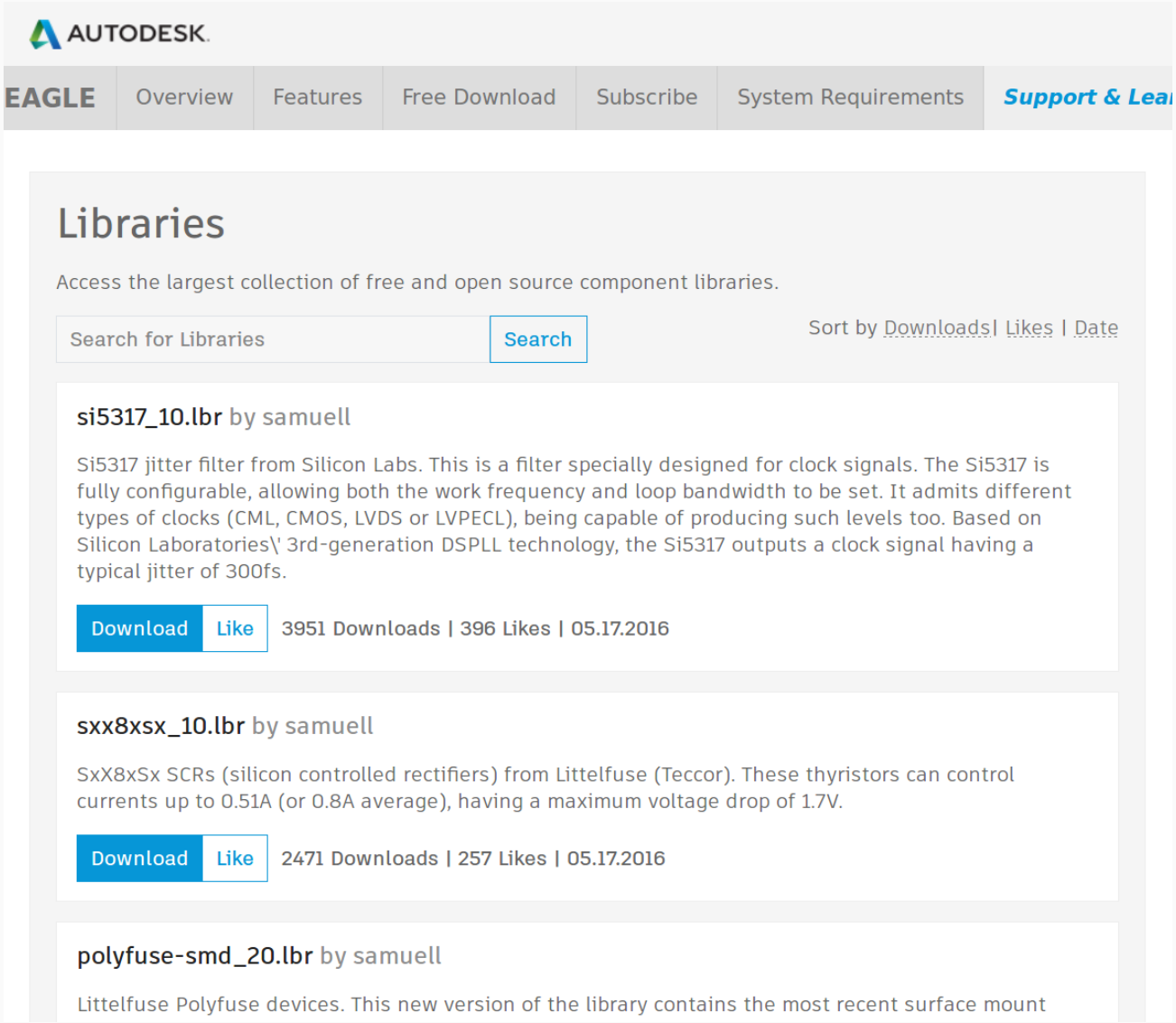
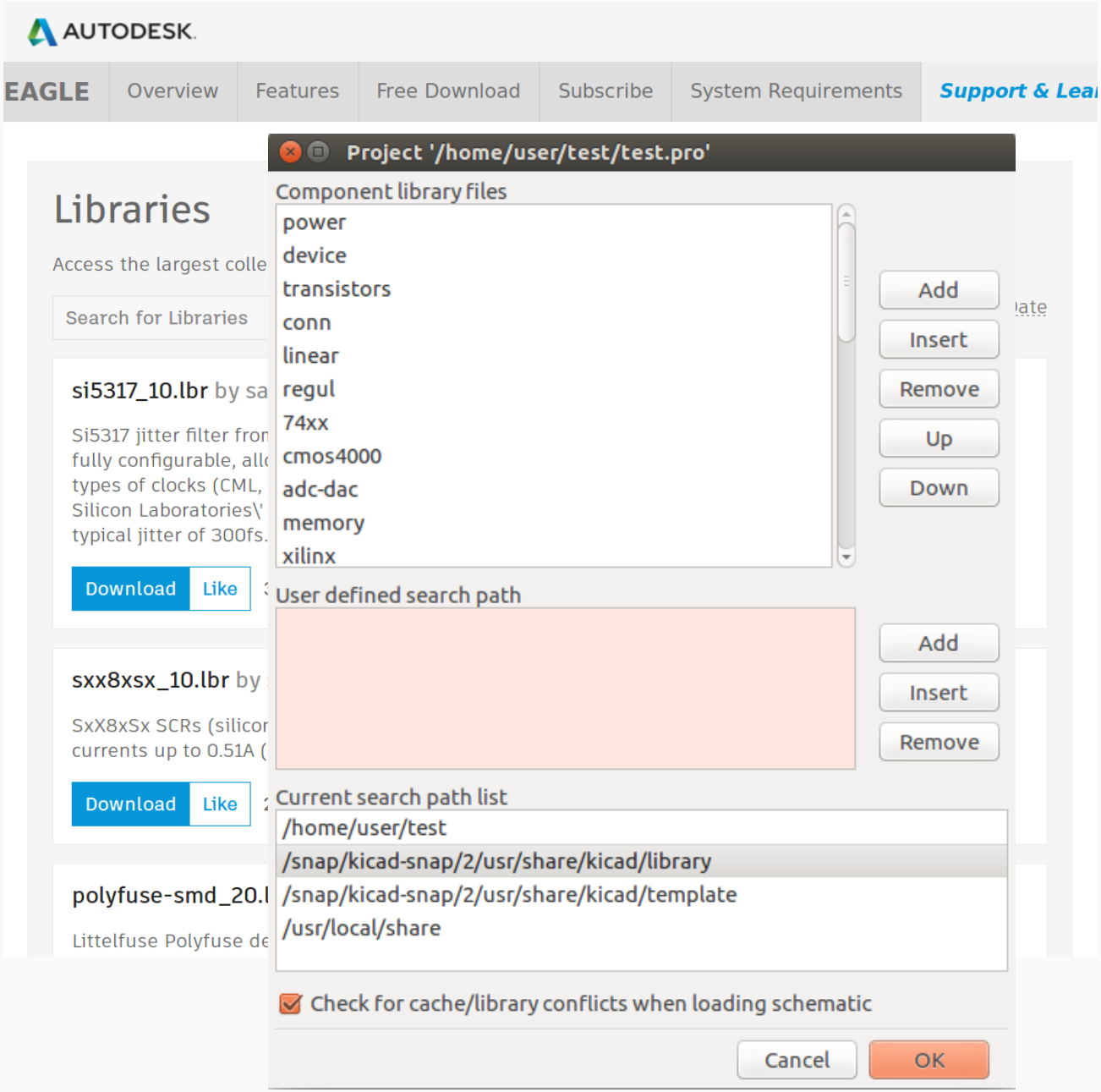
Result

- It's up to the user to manage his libraries (which is a pain)

Library Management



Library Management



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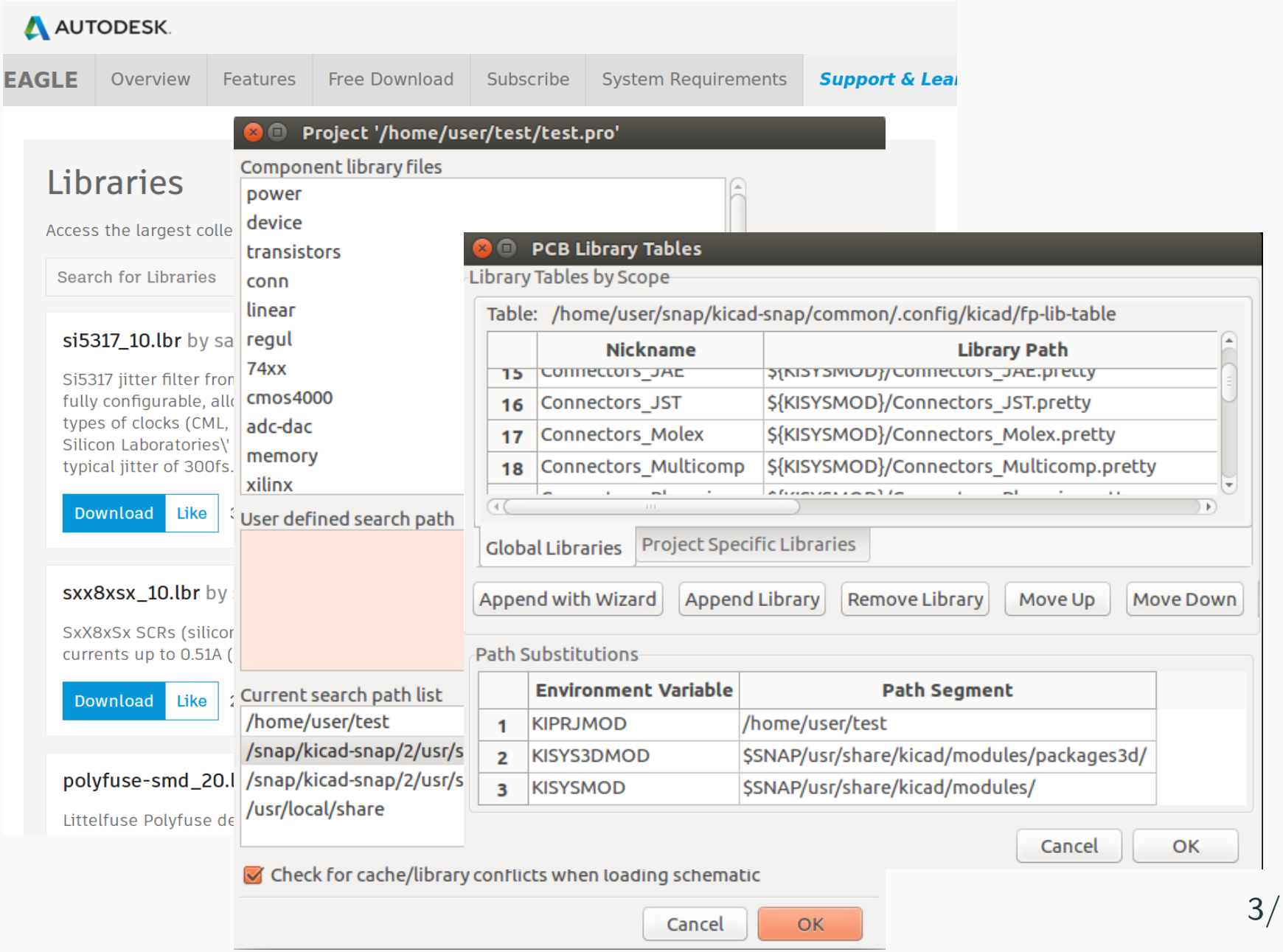

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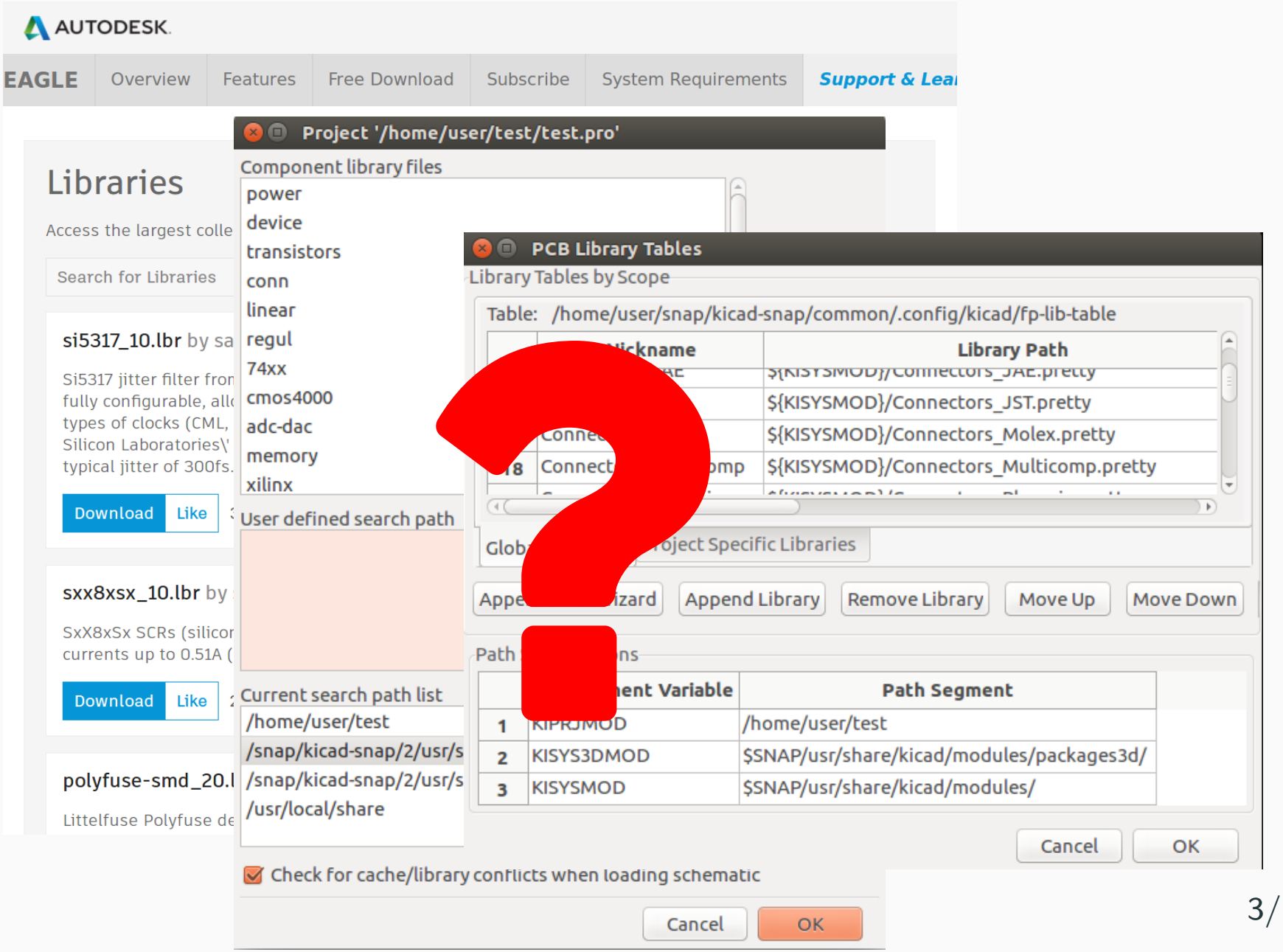
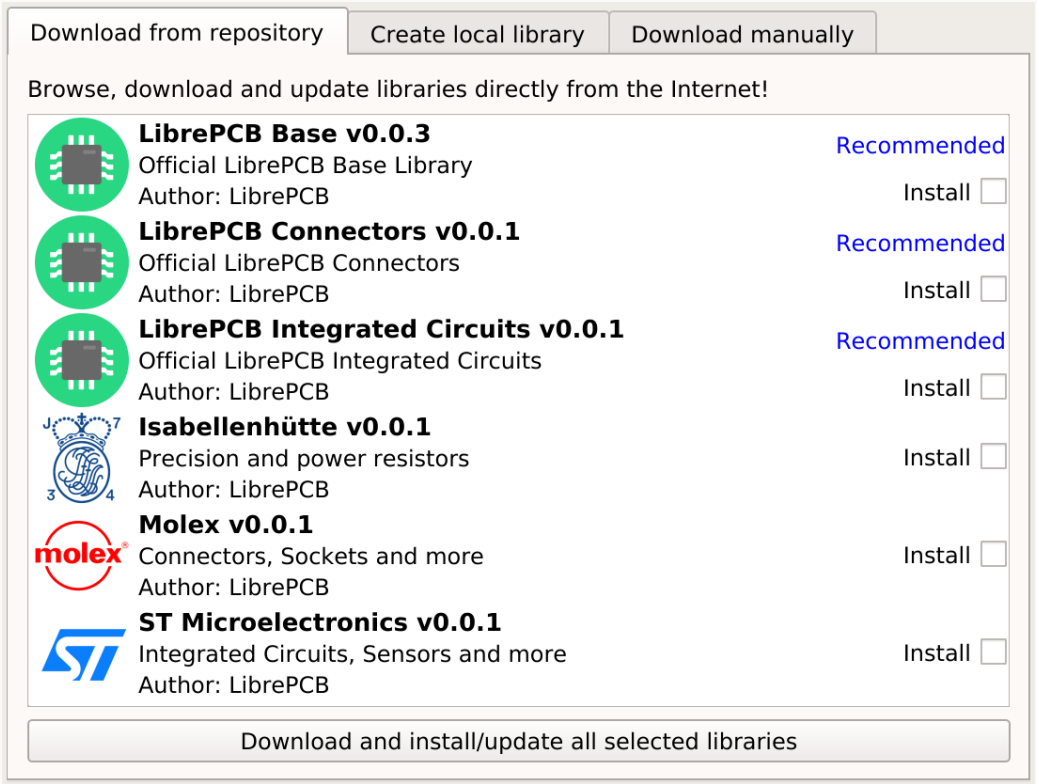


Library Management

Solution

- Integrated library manager with dependency management
- Libraries can contain any entity type (symbols, footprint, ...)
- The application handles basically **everything** for you



Library References

Problem

- Everything is referenced by name
- References across libraries not possible in some tools

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- Name conflicts because they are not unique
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Solution

- Every entity is identified by a random UUID
- References always by UUID, never by name
- Entities can be referenced across different libraries

Library References

```
(librepcb_symbol
 (uuid f0061936-5169-49c9-bfa5-4efc8108cd1c)
 (name "Connector 1x4")
 ...
 (pin 169d6728-7108-4600-aa48-765711db01bc (name "1")
  (pos -20.32 40.64) (rot 0.0) (length 5.08)
 )
 (pin 1c49822e-fd83-452a-a7a6-f4ae1357a0c7 (name "2")
  (pos 20.32 -40.64) (rot 180.0) (length 5.08)
 )
 (pin 208bd2b9-ed07-4df5-b5ab-a89fb03378d5 (name "3")
  (pos 20.32 -38.1) (rot 180.0) (length 5.08)
 )
 (pin 2684075c-566e-43fb-b025-17cf43badaf4 (name "4")
  (pos 20.32 -12.7) (rot 180.0) (length 5.08)
 )
)
```

Symbol Variants

Problem

- Impossible to have different symbols for the same component
  e.g. Resistor: 

Symbol Variants

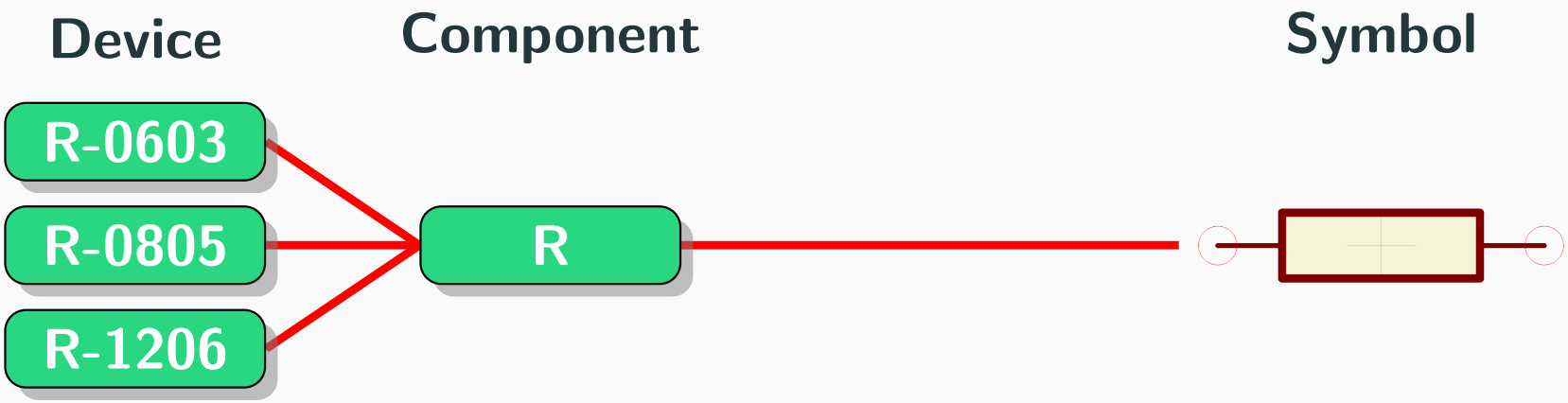
Problem

- Impossible to have different symbols for the same component
  e.g. Resistor: 

Result

- Duplicate components (same functionality, different symbol)

Symbol Variants



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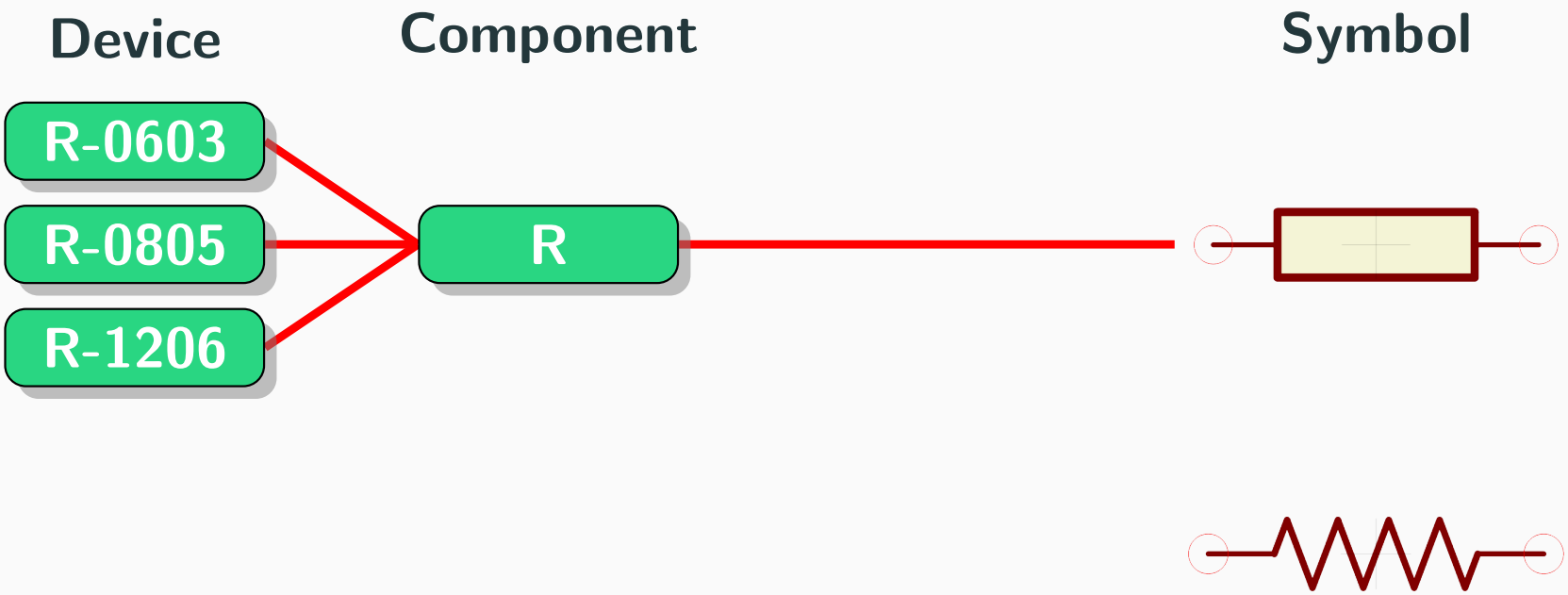

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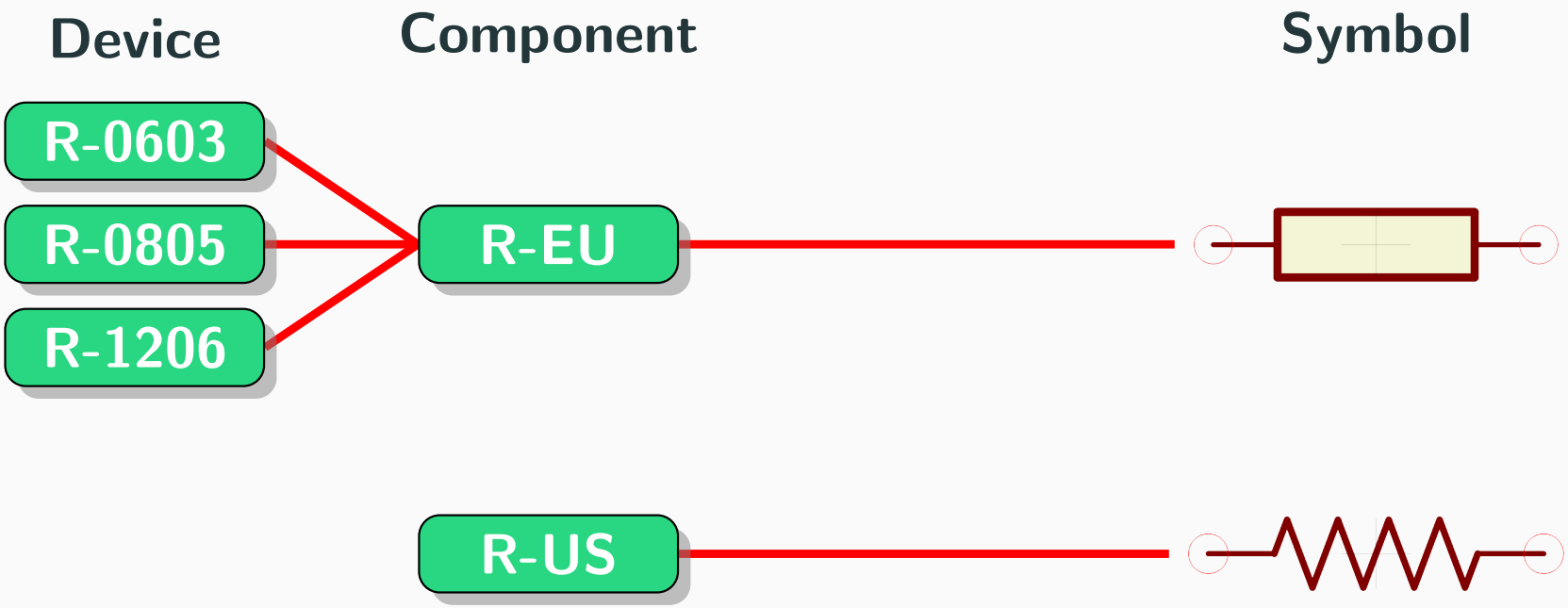

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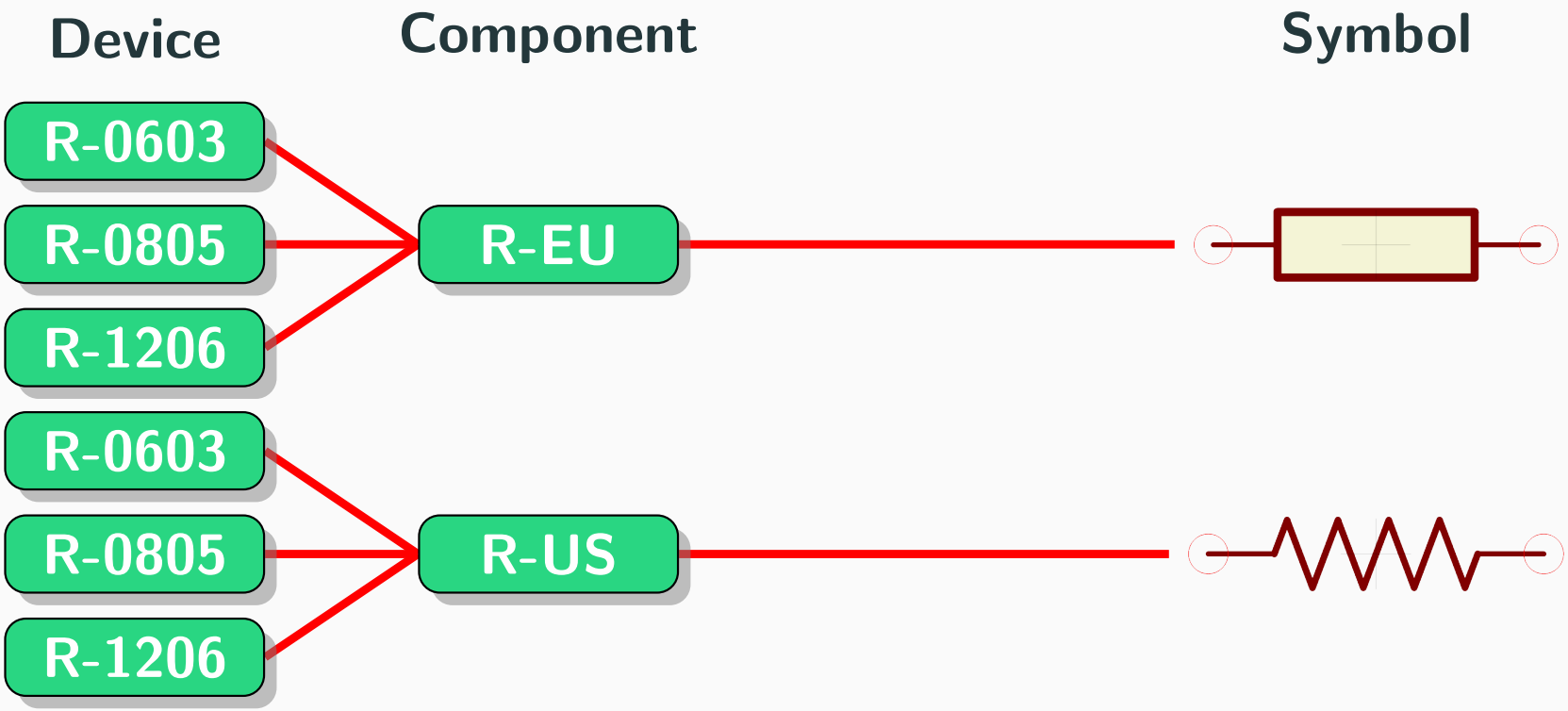

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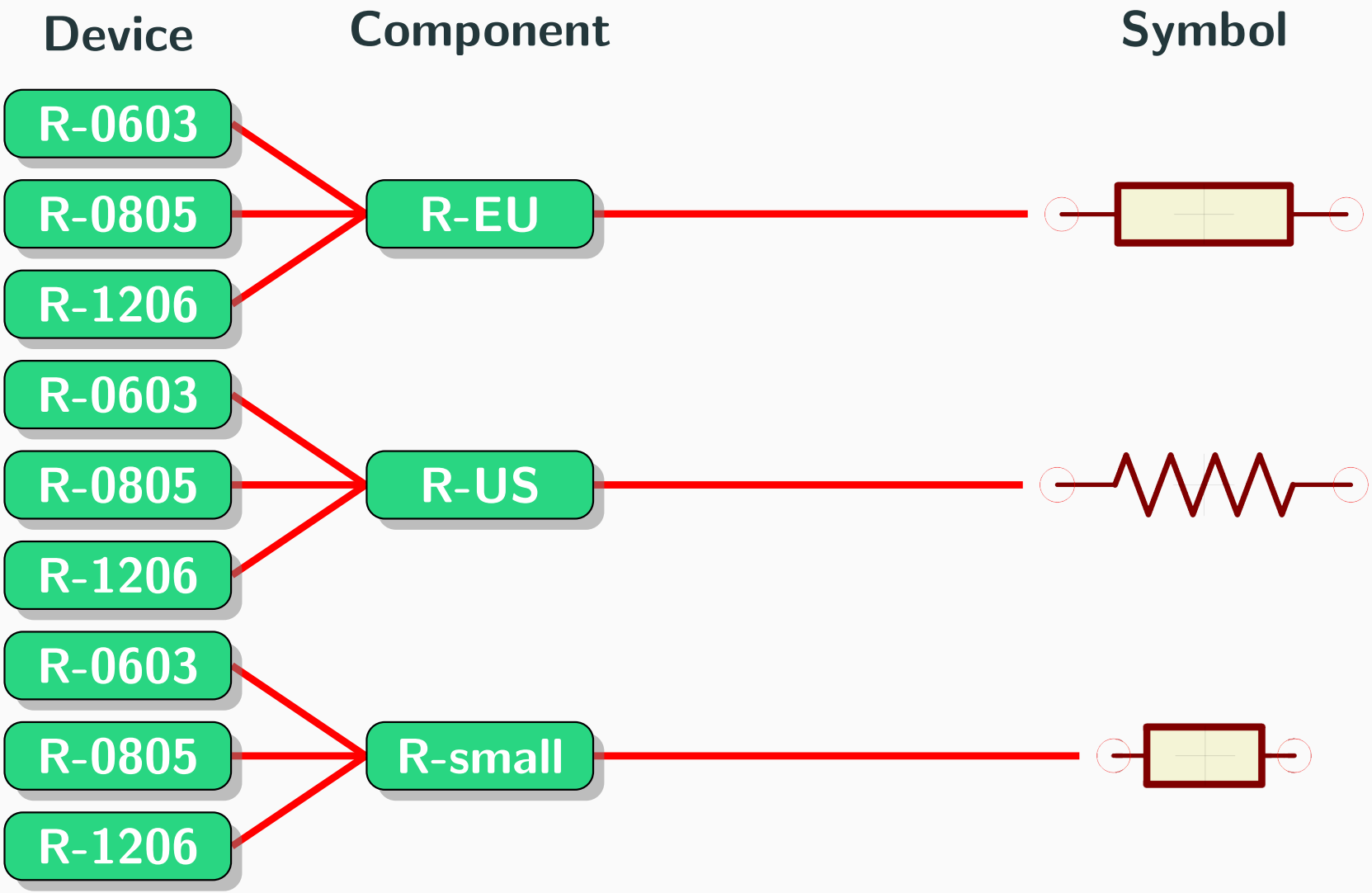

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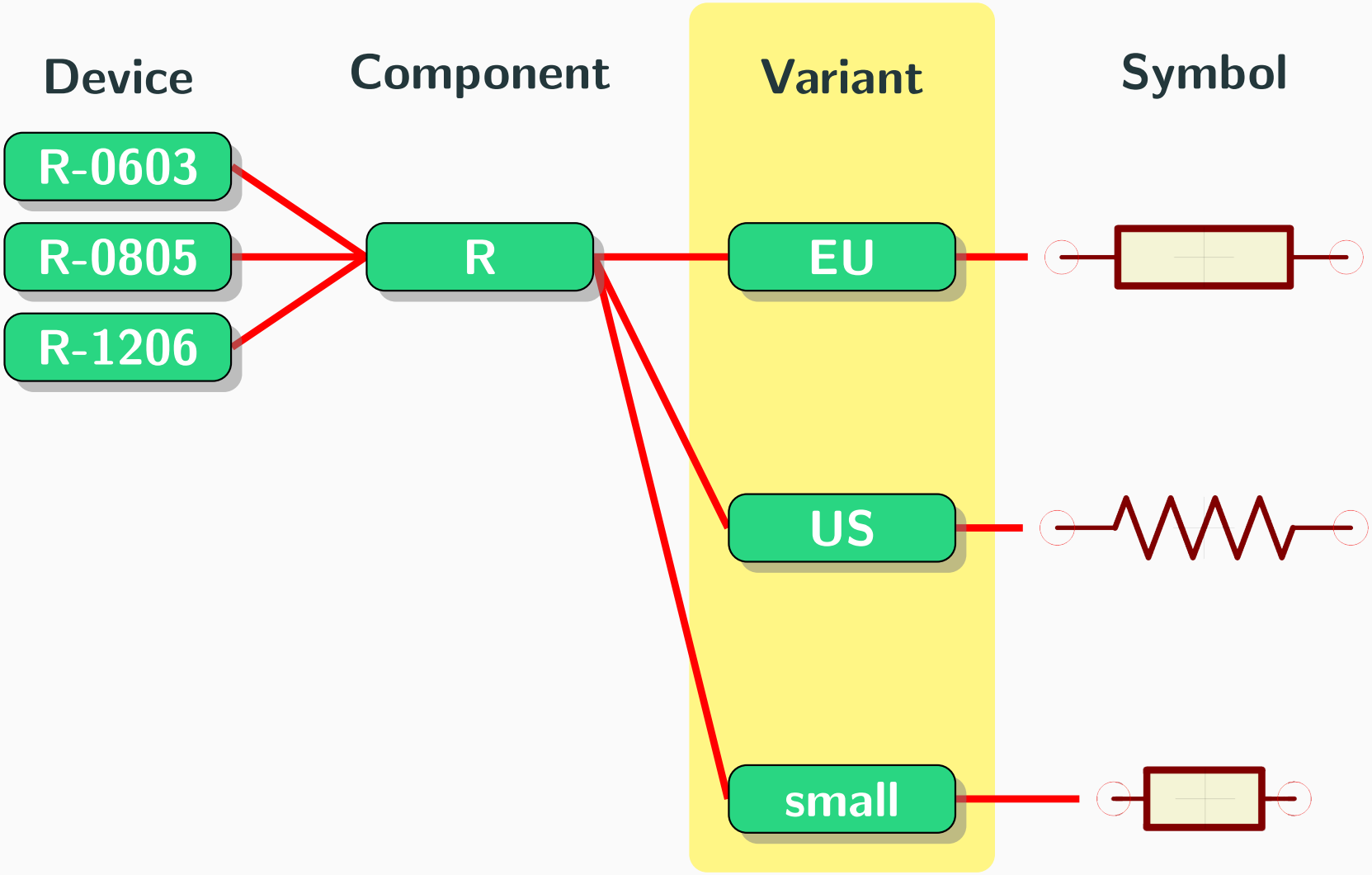
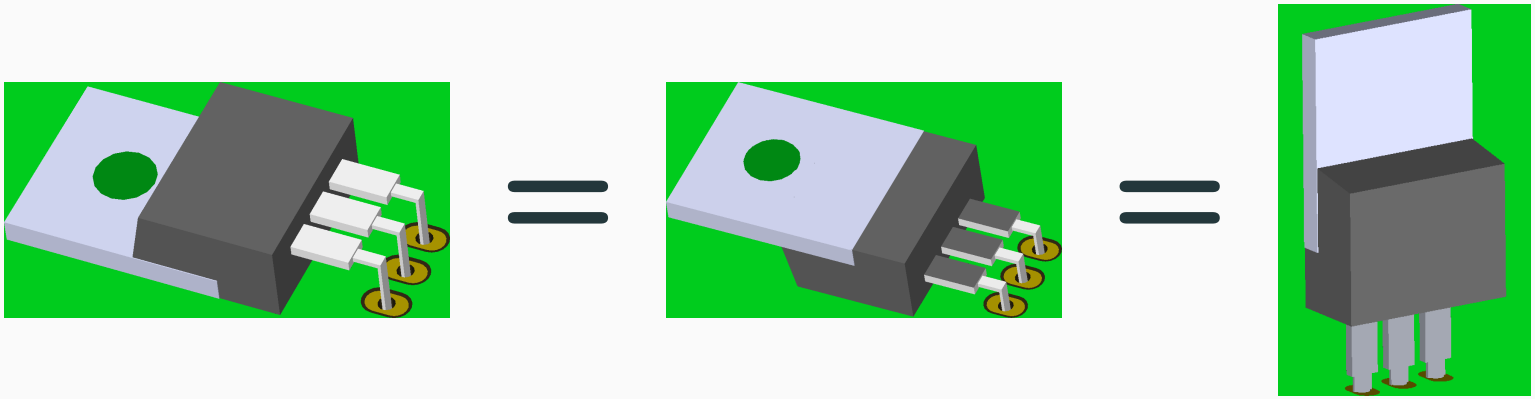


Footprint Variants

Problem

- Libraries do not provide an abstraction layer for **packages**



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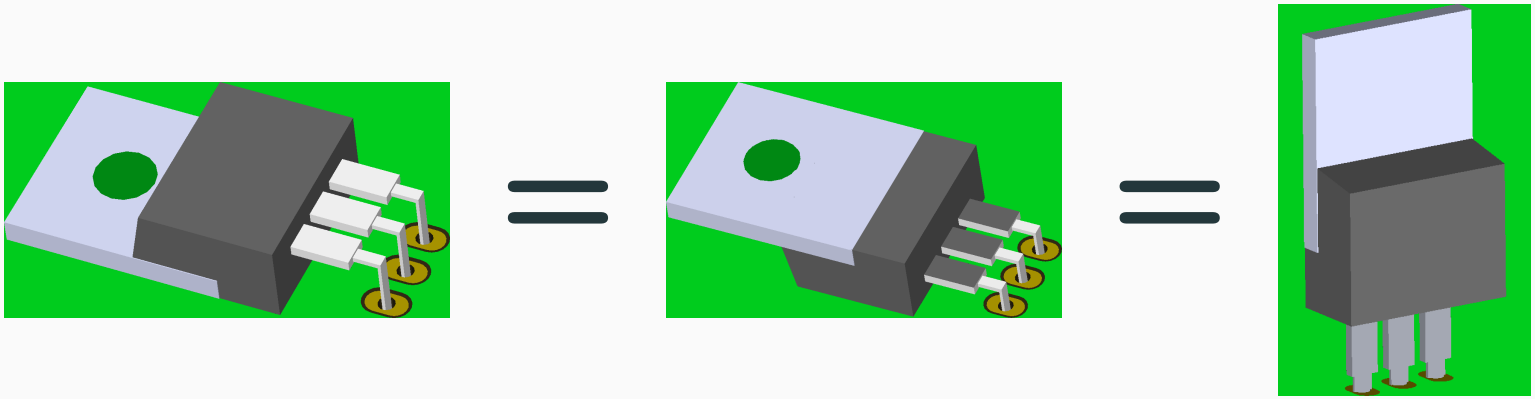
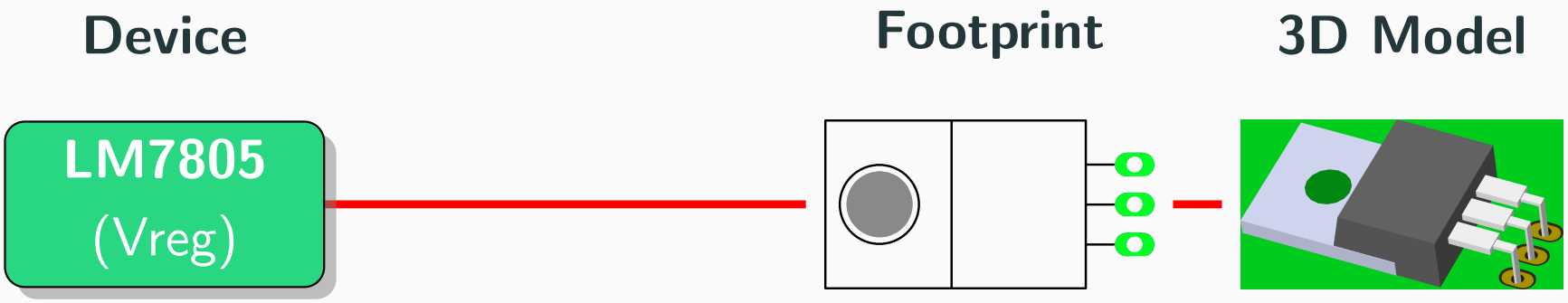


Result

- Devices need to know every footprint variant of their package

Footprint Variants



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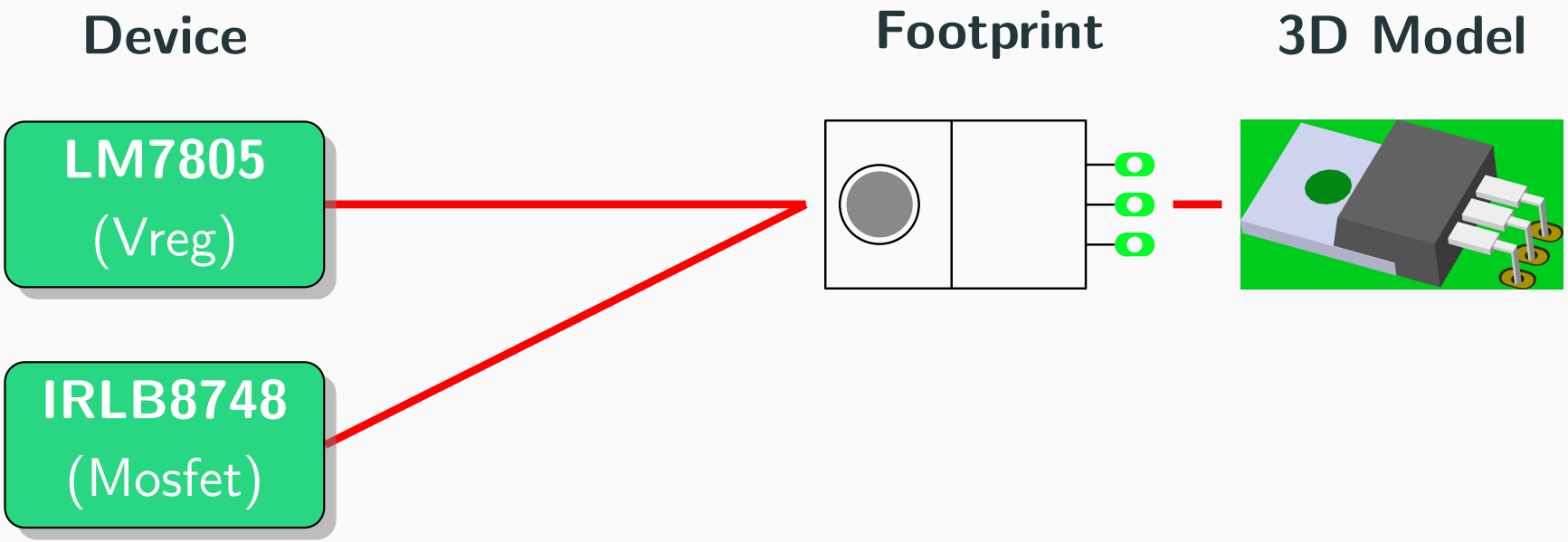

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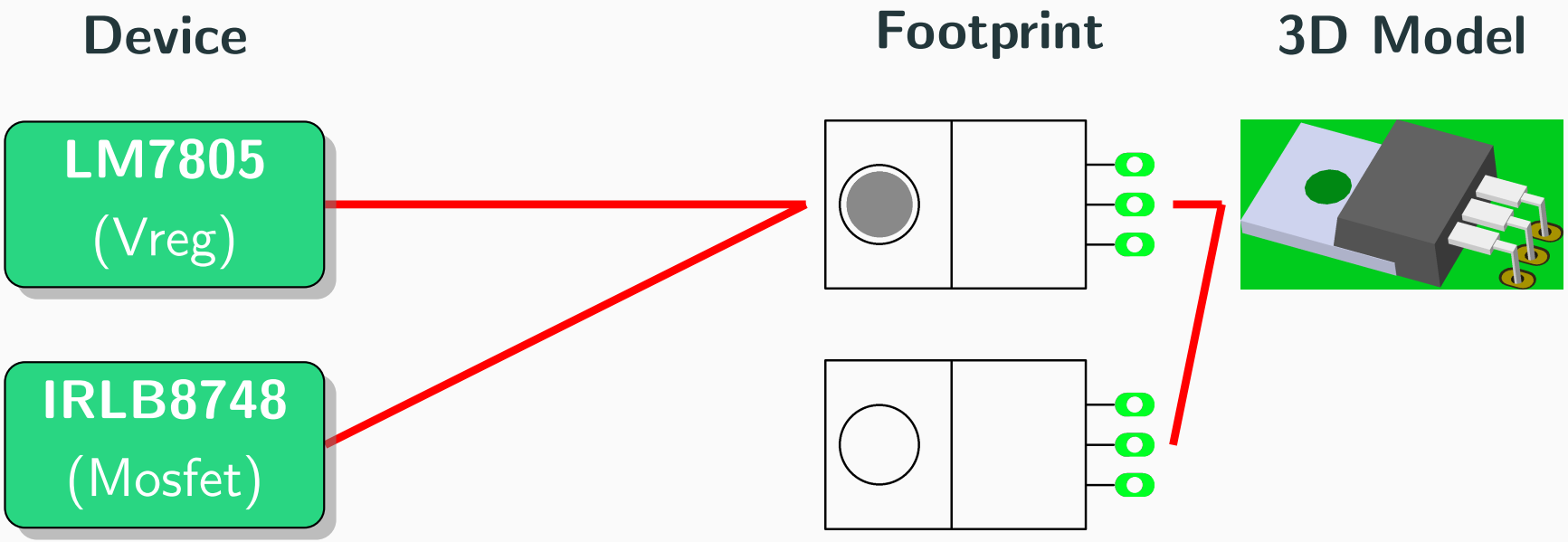

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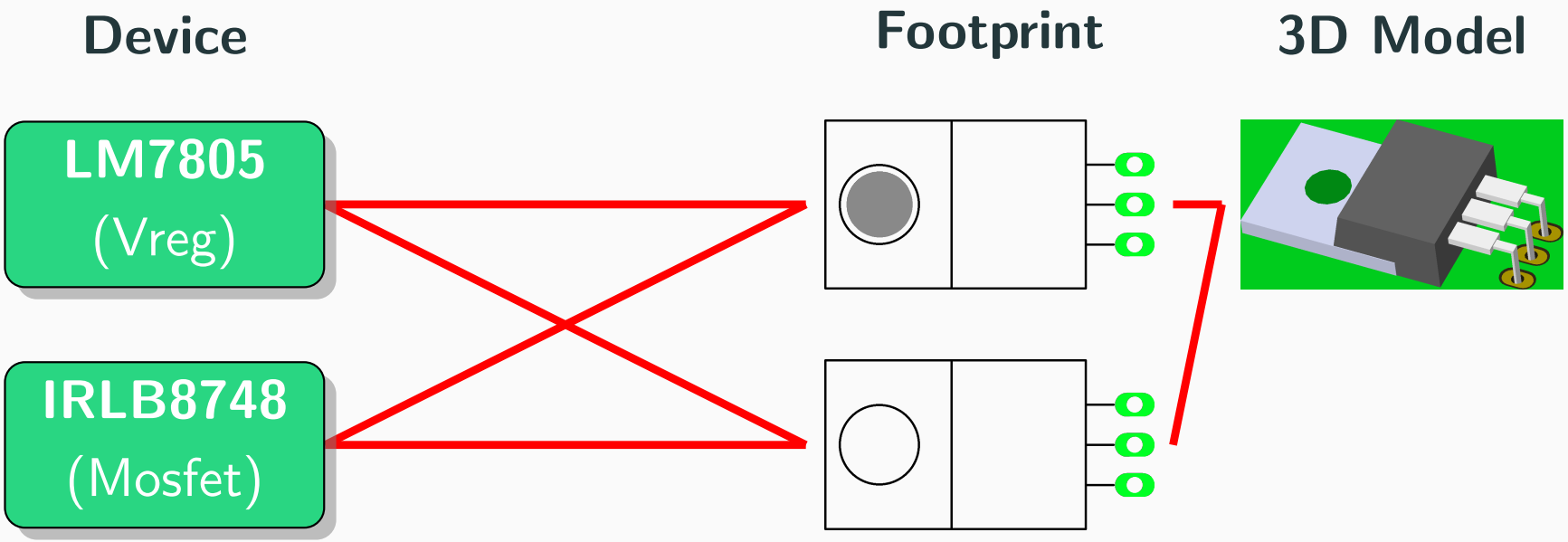

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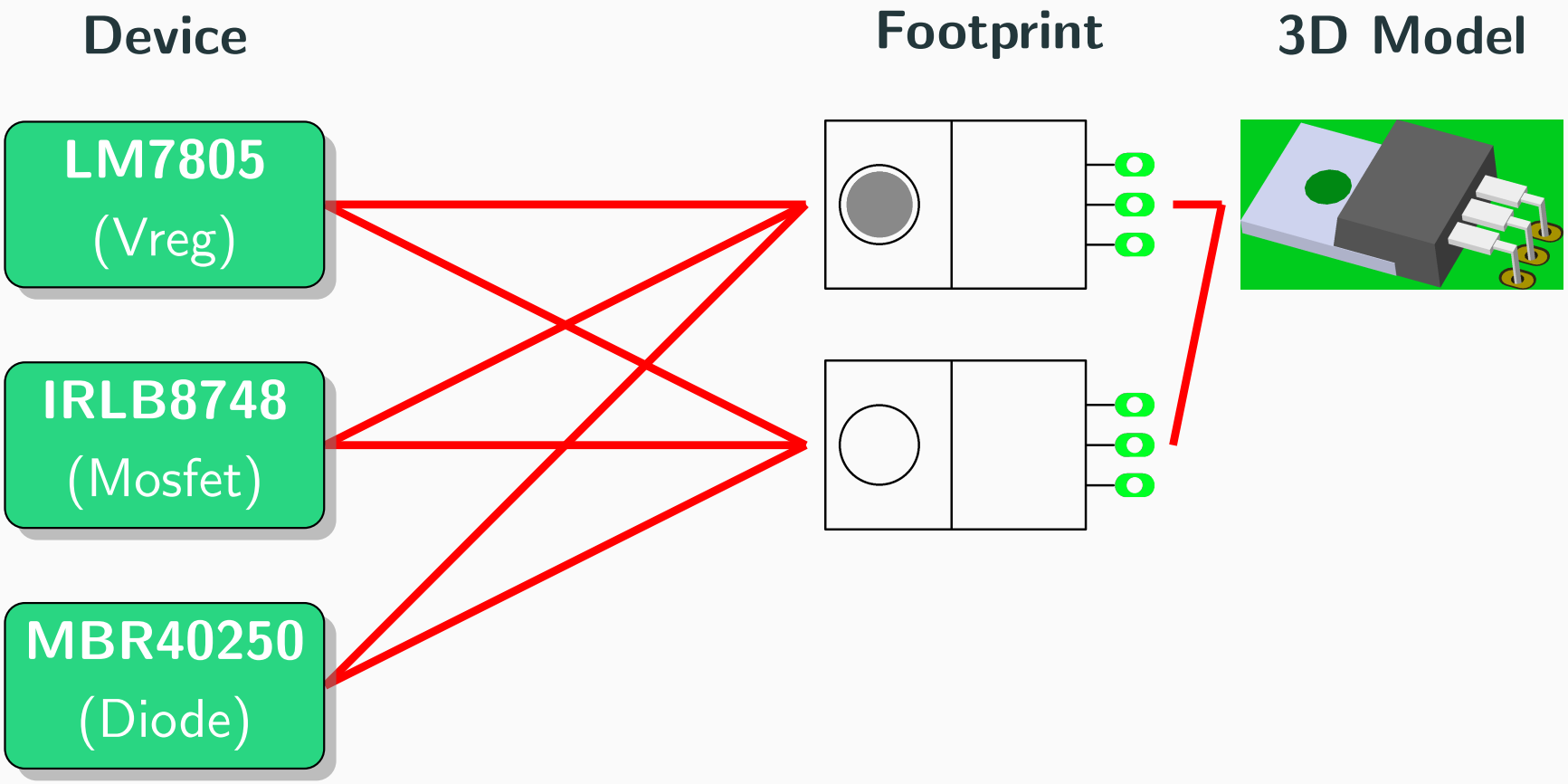

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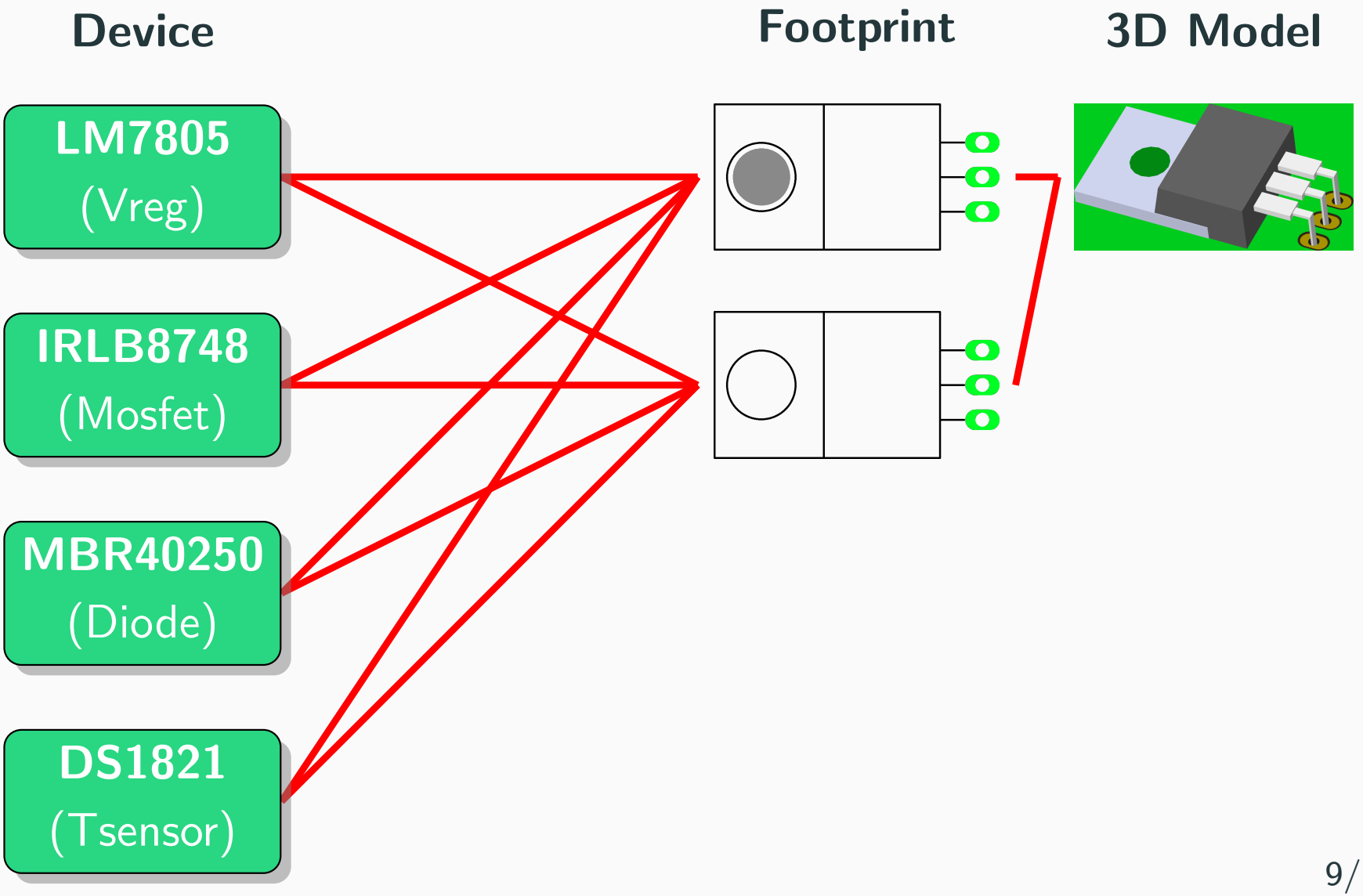

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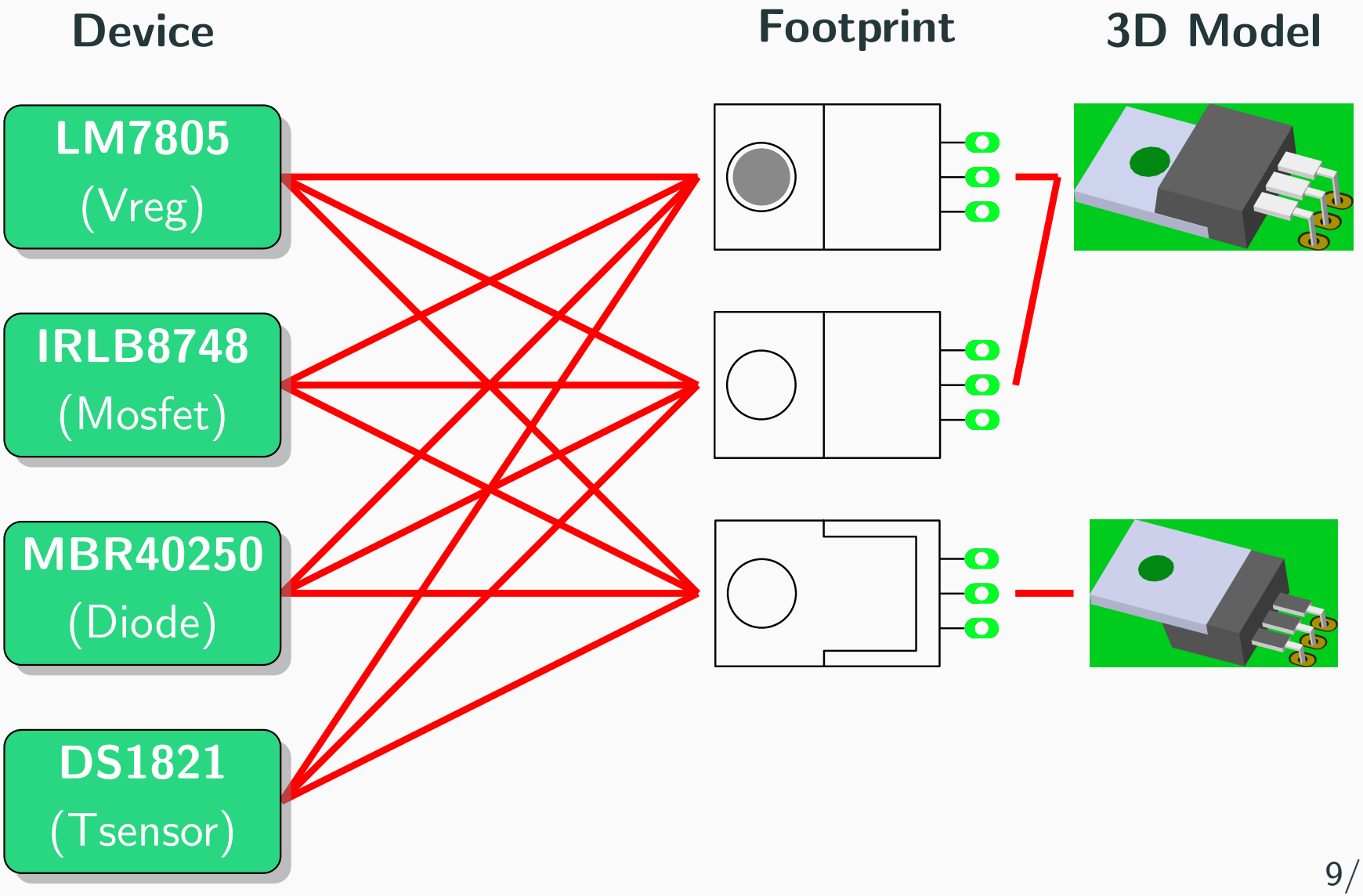

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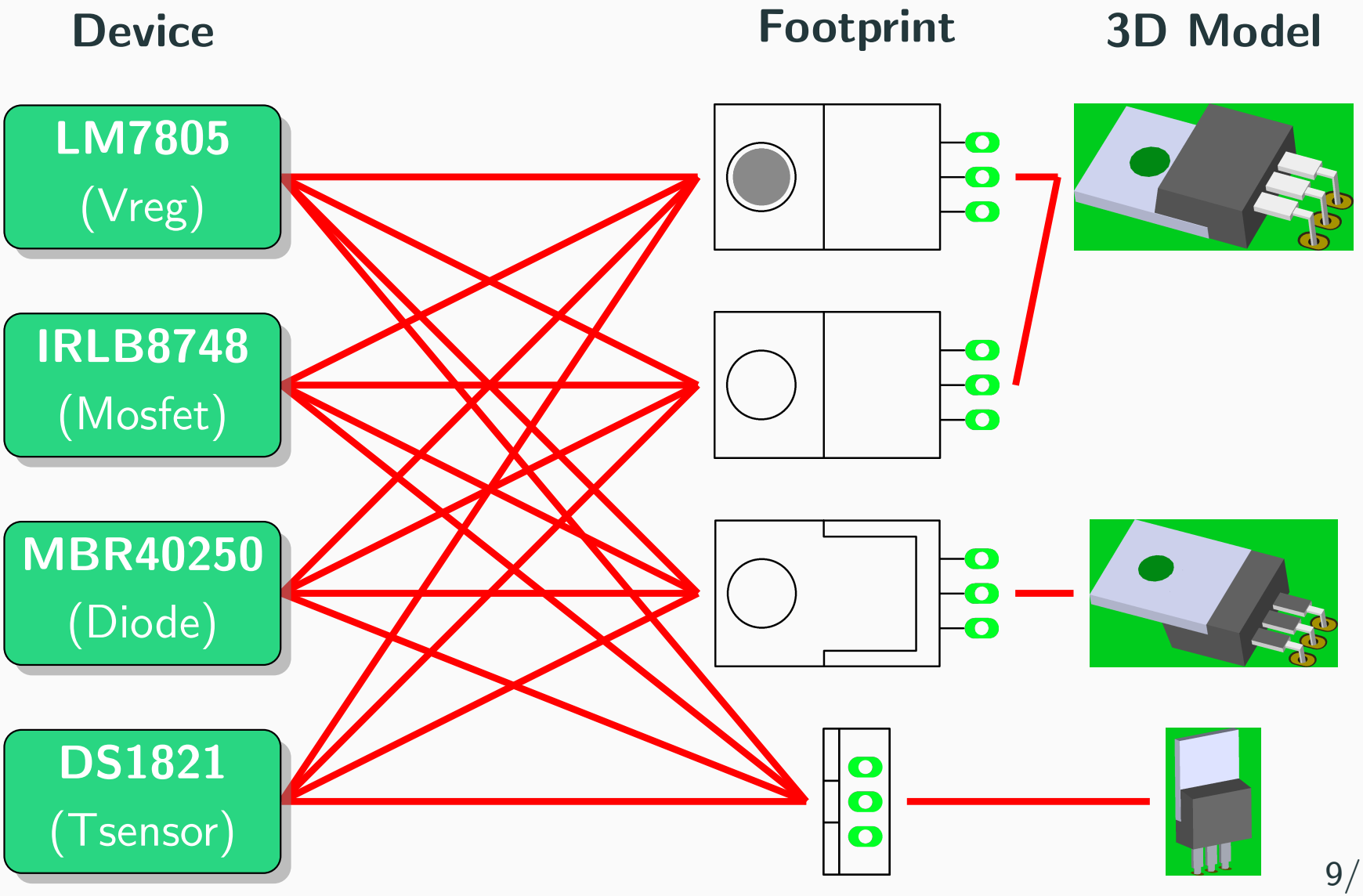

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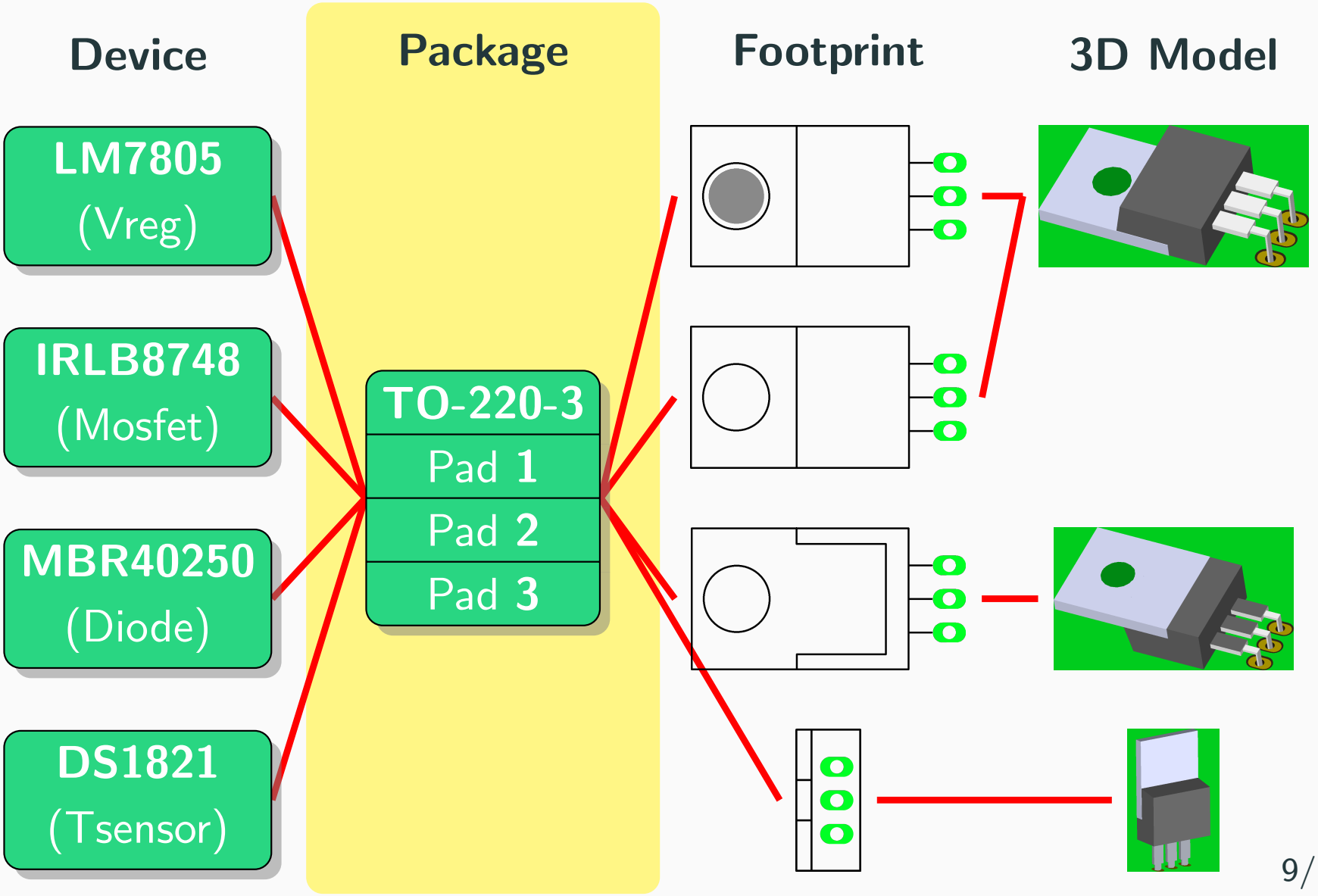

Version Control Systems

Problem

- Important and unimportant data mixed
- Unclear which files to version control

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- Local changes even if nothing modified
- Very large and opaque diffs/commits
- Merging is basically impossible

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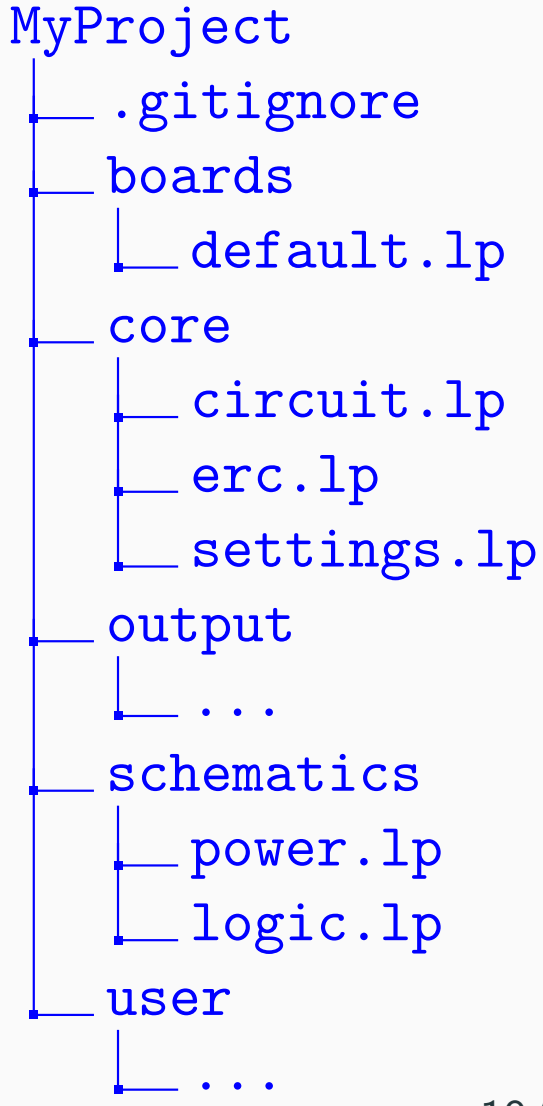
Solution

- Many small files for higher granularity
- Unimportant data strictly separated
- Automatic creation of `.gitignore`



Version Control Systems

```
--- a/test.kicad_pcb
+++ b/test.kicad_pcb
@@ -3,7 +3,7 @@
   (general
     (no_connects 0)
-    (area 41.834999 87.554999 233.755001 153.745001)
+    (area 20.171999 28.969758 233.755001 157.374234)
     (drawings 4)
@@ -21,7 +21,7 @@
     (36 B.SilkS user)
-    (37 F.SilkS user)
+    (37 F.SilkS user hide)
     (38 B.Mask user hide)
@@ -62,7 +62,7 @@
     (aux_axis_origin 0 0)
-    (visible_elements FFFC4601)
+    (visible_elements FFFC4609)
     (pcbplotparams
```

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- Files are **not really** human readable

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Result

- Diffs are very hard to understand
- Limited use of version control systems

Version Control Systems

Problem

- Files are **not really** human readable

Result

- Diffs are very hard to understand
- Limited use of version control systems

Solution

- Don't just consider text-based file formats as human readable!
- Control every tiny detail of the generated files
- Consider opaque parts of files as bugs and fix them

Project Status

Library Management		
Library Editor		
Schematic Editor		(except missing copy&paste)
Board Editor		(no planes, airwires, DRC, ...)
Export (e.g. Gerber)		
Available Libraries		

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Export (e.g. Gerber)		
Available Libraries		

→ File format not yet considered as stable!

→ Breaking changes can (and will) occur!

Getting Started

Nightly builds available for download

- AppImage for Linux
- ZIP with *.exe for Windows

Documentation

`https://docs.librepcb.org/getting_started/`

Demo Time!

Next Steps

Priority 1: First stable release

- Stabilize file format (last breaking changes)
- Make board editor usable (planes, airwires, fonts, DRC)
- Prepare first stable release (fix bugs, polish GUI, ...)

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Priority 2: Add more functionality

- Clipboard
- Hierarchical sheets
- 3D board viewer
- ...

Contributors welcome!

https://github.com/LibrePCB/LibrePCB/blob/master/CONTRIBUTING.md

- Participate in issues
- Open pull requests
- Improve documentation
- Donate (Patreon or Bitcoin)

Thank you!

http://librepcb.org